



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL10X226MR8NUNE

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 22 µF, 4V, ±20%, X6S, 0603

A. Samsung Part Number

<u>CL</u> <u>10</u> <u>X</u> <u>226</u> <u>M</u> <u>R</u> <u>8</u> <u>N</u> <u>U</u> <u>N</u> <u>E</u> ① ② ③ ④ ⑤ ⑥ ⑦ 8 ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor		
② Size	0603 (inch code)	L: 1.6 ± 0.25 mm W: 0.8 ± 0.25 mm	
③ Dielectric	X6S	Inner electrode Ni	
Capacitance	22 μF	Termination Cu	
⑤ Capacitance	±20 %	Plating Sn 100% (Pb Free)	
tolerance		9 Product 0603 Size dimension spec	
6 Rated Voltage	4 V	Special Reserved for future use	
7 Thickness	0.8 ± 0.25 mm	① Packaging Embossed Type, 7" reel	

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition		
Capacitance	Within specified tolerance	120Hz ±20% 0.5±0.1Vrms		
Tan δ (DF)	0.1 max.			
Insulation	10,000Mohm or 50Mohm⋅μF	Rated Voltage 60~120 sec.		
Resistance	Whichever is Smaller			
Appearance	No abnormal exterior appearance	Microscope (×10)		
Withstanding	No dielectric breakdown or	250% of the rated voltage		
Voltage	mechanical breakdown			
Temperature	X6S			
Characteristics	(From -55℃ to 105℃, Capacitance change should be within ±22%)			
Adhesive Strength	No peeling shall be occur on the	500g·F, for 10±1 sec.		
of Termination	terminal electrode			
Bending Strength	Capacitance change : within ±12.5%	Bending to the limit (1mm)		
		with 1.0mm/sec.		
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder		
is to be soldered newly		245±5℃, 3±0.3sec.		
		(preheating : 80~120 ℃ for 10~30sec.)		
Resistance to	Capacitance change: within ±7.5%	Solder pot : 270±5 ℃, 10±1sec.		
Soldering heat	Tan δ, IR : initial spec.			

	Performance	Test condition
Vibration Test	Capacitance change: within ±5%	Amplitude : 1.5mm
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)
		2hours × 3 direction (x, y, z)
Moisture	Capacitance change: within ±12.5%	With rated voltage
Resistance	Tan δ: 0.25 max	40±2℃, 90~95%RH, 500+12/-0hrs
	IR: $8.8 \text{M}\Omega \cdot \mu\text{F}$ or Over	
High Temperature	Capacitance change: within ±12.5%	With 100% of the rated voltage
Resistance	Tan δ : 0.25 max	Max. operating temperature
	IR: 17.7MΩ·μF or Over	
		1000+48/-0hrs
Temperature	Capacitance change: within ±10%	1 cycle condition
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25 °C
		→ Max. operating temperature → 25°C
		5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}$ C, 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.