Low Power Oscillator for High Frequency Applications



■ Features, Benefits and Applications

- Lowest power, high frequency oscillator with 6.3 mA typical active current
- 125MHz to 150MHz frequency range
- LVCMOS/LVTTL compatible output
- Excellent frequency stability over temperature, ±20 PPM
- Ultra low standby current, 1.2 µA
- Standby or output enable modes
- Four industry-standard packages: 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 mm
- Drop-in replacement of quartz
- Ultra short lead time
- All-silicon device with outstanding reliability of 2 FIT (10x improvement over quartz-based devices), enhancing system mean-time-to-failure (MTBF)
- Ideal for high frequency applications in networking, storage, computer servers and communications
- Ideal for high speed protocals: GPON, EPON, Ethernet, SATA/SAS, DDR, PCI

■ Specifications

Electrical Characteristics

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Condition | |
|-----------------------------|----------|------|------|------|------|---------------------------------------------------------------------------------------|--|
| Output Frequency Range | f | 125 | - | 150 | MHz | | |
| Frequency Stability | F_stab | -20 | - | +20 | PPM | Inclusive of: Initial stability, operating temperature, rated power, | |
| | | -25 | _ | +25 | PPM | supply voltage change, load change, shock and vibration | |
| | | -50 | - | +50 | PPM | ± 20 PPM available in extended commercial temperature only | |
| Aging | Ag | -1.0 | _ | 1.0 | PPM | 1st year at 25 ℃ | |
| Operating Temperature Range | T_use | -20 | - | +70 | ∞ | Extended Commercial | |
| | | -40 | - | +85 | ℃ | Industrial | |
| Supply Voltage | Vdd | 2.25 | 2.5 | 2.75 | V | | |
| | | 2.52 | 2.8 | 3.08 | V | | |
| | | 2.97 | 3.3 | 3.63 | V | | |
| Current Consumption | ldd | _ | 6.7 | 8 | mA | No load condition, f = 125 MHz, Vdd = 3.3 V | |
| | | _ | 6.2 | 7 | mA | No load condition, f = 125 MHz, Vdd = 2.5 V or 2.8 V | |
| Standby Current | I_std | _ | 2.4 | 4.3 | μΑ | ST = GND, Vdd = 3.3 V, Output is Weakly Pulled Down | |
| | | _ | 1.2 | 2.2 | μΑ | ST = GND, Vdd = 2.5 or 2.8 V, Output is Weakly Pulled Down | |
| Duty Cycle | DC | 40 | 50 | 60 | % | All Vdds | |
| Rise/Fall Time | Tr, Tf | - | 1 | 2 | ns | 20% - 80% Vdd=2.5 V, 2.8 V or 3.3 V, 15 pf load | |
| Output Voltage High | VOH | 90% | _ | _ | Vdd | IOH = -4 mA (Vdd = 3.3 V) IOH = -3 mA (Vdd = 2.8 V and Vdd = 2.5 V) | |
| Output Voltage Low | VOL | - | - | 10% | Vdd | IOL = 4 mA (Vdd = 3.3 V) IOL = 3 mA (Vdd = 2.8 V and Vdd = 2.5 V) | |
| Output Load | Ld | - | - | 15 | pF | At 125MHz and max supply voltage. Contact SiTime for higher outpuload option | |
| Input Voltage High | VIH | 70% | - | - | Vdd | Pin 1, OE or ST | |
| Input Voltage Low | VIL | _ | - | 30% | Vdd | Pin 1, OE or ST | |
| Startup Time | T_start | _ | - | 10 | ms | Measured from the time Vdd reaches its rated minimum value | |
| Resume Time | T_resume | - | 3.0 | 3.8 | ms | Measured from the time ST pin crosses 50% threshold | |
| RMS Period Jitter | T_jitt | - | - | 4.5 | ps | f = 125 MHz, Vdd = 2.5 V, 2.8 V or 3.3 V | |
| RMS Phase Jitter (random) | T_phj | - | 0.5 | - | ps | f = 125 MHz, Integration bandwidth = 900 kHz to 7.5 MHz, VDD = 2.5 V, 2.8 V, or 3.3 V | |

SiTime Corporation 990 Almanor Avenue Sunnyvale, CA 94085 www.sitime.com Revised November 11, 2010

Low Power Oscillator for High Frequency Applications



■ Specifications (Cont.)

Pin Description Tables

| Pin #1 Functionality OE |
|------------------------------------------------------------|
| H or Open: specified frequency output |
| L: output is high impedance |
| ST |
| H or Open: specified frequency output |
| L: output is low level (weak pull down). Oscillation stops |

| Pin Map | | | | |
|---------|------------|--|--|--|
| Pin | Connection | | | |
| 1 | OE/ST | | | |
| 2 | GND | | | |
| 3 | CLK | | | |
| 4 | VDD | | | |

Absolute Maximum Table

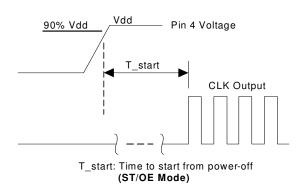
Attempted operation outside the absolute maximum ratings of the part may cause permanent damage to the part. Actual performance of the IC is only guaranteed within the operational specifications, not at absolute maximum ratings.

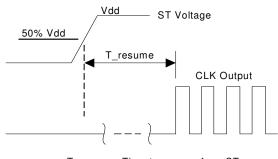
| Parameter | Min. | Max. | Unit |
|----------------------------------------------------------------------|--------|------|-------|
| Storage Temperature | -65 | 150 | ℃ |
| VDD | -0.5 | 4 | V |
| Electrostatic Discharge | - | 2000 | V |
| Theta JA (with copper plane on VDD and GND) | - | 75 | °C/W |
| Theta JC (with PCB traces of 0.010 inch to all pins) | - | 24 | °C/W |
| Soldering Temperature (follow standard Pb free soldering guidelines) | - | 260 | ℃ |
| Number of Program Writes | - | 1 | NA |
| Program Retention over -40 to 125 °C, Process, VDD (0 to 3.65 V) | 1,000+ | - | years |

Environmental Compliance

| Parameter | Condition/Test Method |
|----------------------------|---------------------------|
| Mechanical Shock | MIL-STD-883F, Method 2002 |
| Mechanical Vibration | MIL-STD-883F, Method 2007 |
| Temperature Cycle | JESD22, Method A104 |
| Solderability | MIL-STD-883F, Method 2003 |
| Moisture Sensitivity Level | MSL1 @ 260℃ |

Startup and Resume Timing Diagram

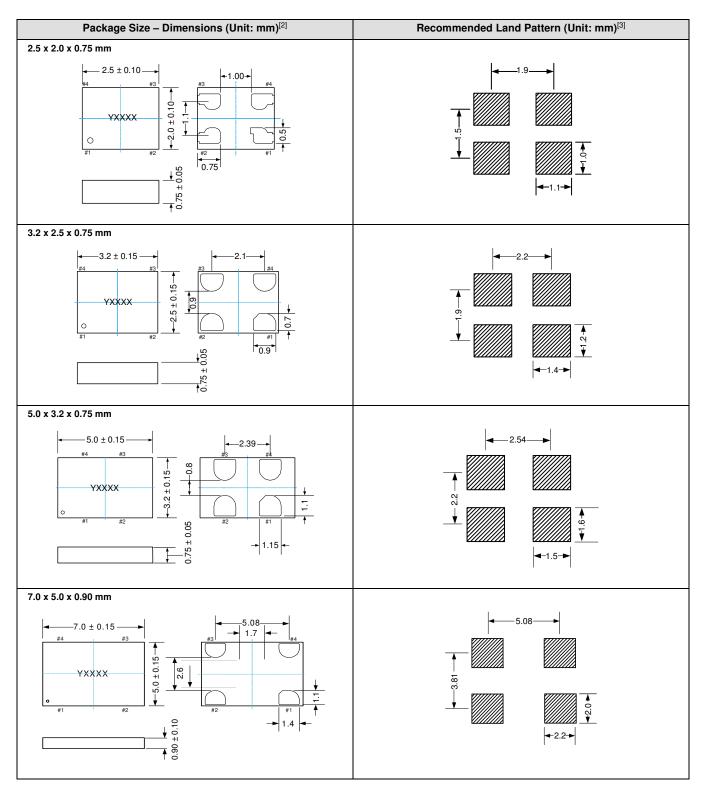




Low Power Oscillator for High Frequency Applications



■ Dimensions and Land Patterns

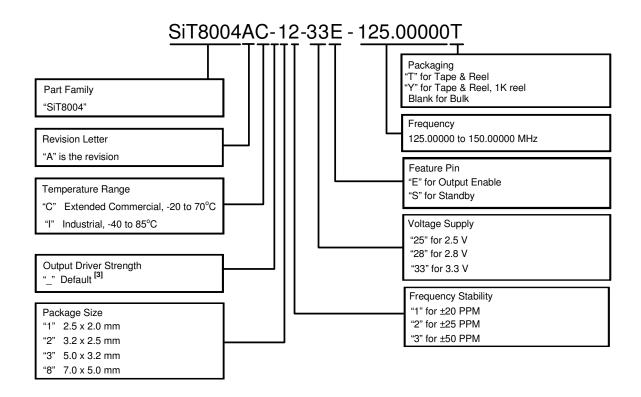


- Y denotes manufacturing origin and XXXX denotes manufacturing lot number. The value of "Y" will depend on the assembly location of the device.
 A capacitor of value 0.1 μF between Vdd and GND is recommended.

Low Power Oscillator for High Frequency Applications



■ Part No. Guide = How to Order



Note:

3. Contact SiTime for different drive strength options for driving higher loads or reducing EMI.

Frequency Stability vs. Temperature Range Options

| F | T | Supply Voltage | | |
|------------------------------|----------------------------------------|----------------|-------|-------|
| Frequency Stability (PPM) | Temperature Range | 2.5 V | 2.8 V | 3.3 V |
| ±20 | C (-20 to +70 °C) | ✓ | ✓ | ✓ |
| | I (-40 to +85 ℃) | - | - | - |
| ±25 | C (-20 to +70 °C) I (-40 to +85 °C) | ✓ | ✓ | ✓ |
| ±50 | C (-20 to +70 °C) I (-40 to +85 °C) | ✓ | ✓ | ✓ |

© SiTime Corporation 2010. The information contained herein is subject to change at any time without notice. SiTime assumes no responsibility or liability for any loss, damage or defect of a Product which is caused in whole or in part by (i) use of any circuitry other than circuitry embodied in a SiTime product, (ii) misuse or abuse including static discharge, neglect or accident, (iii) unauthorized modification or repairs which have been soldered or altered during assembly and are not capable of being tested by SiTime under its normal test conditions, or (iv) improper installation, storage, handling, warehousing or transportation, or (v) being subjected to unusual physical, thermal, or electrical stress.

Disclaimer: SiTime makes no warranty of any kind, express or implied, with regard to this material, and specifically disclaims any and all express or implied warranties, either in fact or by operation of law, statutory or otherwise, including the implied warranties of merchantability and fitness for use or a particular purpose, and any implied warranty arising from course of dealing or usage of trade, as well as any common-law duties relating to accuracy or lack of negligence, with respect to this material, any sitime product and any product documentation. products sold by sitme are not suitable or intended to be used in a life support application or component, to operate nuclear facilities, or in other mission critical applications where human life may be involved or at stake. all sales are made conditioned upon compliance with the critical uses policy set forth below.

CRITICAL USE EXCLUSION POLICY

BUYER AGREES NOT TO USE SITIME'S PRODUCTS FOR ANY APPLICATION OR IN ANY COMPONENTS USED IN LIFE SUPPORT DEVICES OR TO OPERATE NUCLEAR FACILITIES OR FOR USE IN OTHER MISSION-CRITICAL APPLICATIONS OR COMPONENTS WHERE HUMAN LIFE OR PROPERTY MAY BE AT STAKE.

SiTime owns all rights, title and interest to the intellectual property related to SiTime's products, including any software, firmware, copyright, patent, or trademark. The sale of SiTime products does not convey or imply any license under patent or other rights. SiTime retains the copyright and trademark rights in all documents, catalogs and plans supplied pursuant to or ancillary to the sale of products or services by SiTime. Unless otherwise agreed to in writing by SiTime, any reproduction, modification, translation, compilation, or representation of this material shall be strictly prohibited.