

# FGPF30N30T

## 300V, 30A PDP Trench IGBT

### Features

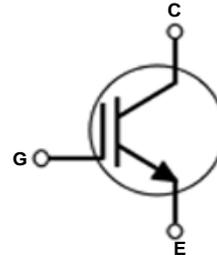
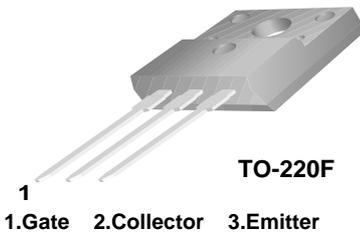
- High current capability
- Low saturation voltage:  $V_{CE(sat)} = 1.4V @ I_C = 20A$
- High input impedance
- Fast switching
- RoHS compliant

### General Description

Using Novel Trench IGBT Technology, Fairchild's new series of trench IGBTs offer the optimum performance for PDP applications where low conduction and switching losses are essential.

### Applications

- PDP System



### Absolute Maximum Ratings

Symbol	Description	Ratings	Units
$V_{CES}$	Collector to Emitter Voltage	300	V
$V_{GES}$	Gate to Emitter Voltage	$\pm 30$	V
$I_{C\ pulse\ (1)}$	Pulsed Collector Current @ $T_C = 25^\circ C$	80	A
$P_D$	Maximum Power Dissipation @ $T_C = 25^\circ C$	44.6	W
	Maximum Power Dissipation @ $T_C = 100^\circ C$	17.8	W
$T_J$	Operating Junction Temperature	-55 to +150	$^\circ C$
$T_{stg}$	Storage Temperature Range	-55 to +150	$^\circ C$
$T_L$	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	2.8	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	62.5	$^\circ C/W$

**Notes :**

(1) Repetitive tesse, Pulse width = 100usec, Duty = 0.1

\* $I_{C\_pulse}$  limited by max  $T_J$

### Package Marking and Ordering Information

Device Marking	Device	Package	Packaging Type	Qty per Tube	Max Qty per Box
FGPF30N30T	FGPF30N30TTU	TO-220F	Rail / Tube	50ea	-

### Electrical Characteristics of the IGBT T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{CES}$	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	300	-	-	V
$\Delta BV_{CES} / \Delta T_J$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	-	0.26	-	V/°C
$I_{CES}$	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	100	$\mu A$
$I_{GES}$	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	±400	nA
<b>On Characteristics</b>						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 250\mu A, V_{CE} = V_{GE}$	3.0	4.5	5.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 10A, V_{GE} = 15V$	-	1.2	1.5	V
		$I_C = 20A, V_{GE} = 15V$	-	1.5	-	V
		$I_C = 30A, V_{GE} = 15V, T_C = 25^\circ C$	-	1.7	-	V
		$I_C = 30A, V_{GE} = 15V, T_C = 125^\circ C$	-	1.6	-	V
<b>Dynamic Characteristics</b>						
$C_{ies}$	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V, f = 1MHz$	-	1540	--	pF
$C_{oes}$	Output Capacitance		-	65	--	pF
$C_{res}$	Reverse Transfer Capacitance		-	55	--	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 200V, I_C = 20A, R_G = 20\Omega, V_{GE} = 15V, \text{Inductive Load}, T_C = 25^\circ C$	-	22	--	ns
$t_r$	Rise Time		-	33	--	ns
$t_{d(off)}$	Turn-Off Delay Time		-	130	--	ns
$t_f$	Fall Time		-	180	300	ns
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 200V, I_C = 20A, R_G = 20\Omega, V_{GE} = 15V, \text{Inductive Load}, T_C = 125^\circ C$	-	21	--	ns
$t_r$	Rise Time		-	34	--	ns
$t_{d(off)}$	Turn-Off Delay Time		-	140	--	ns
$t_f$	Fall Time		-	260	--	ns
$Q_g$	Total Gate Charge	$V_{CE} = 200V, I_C = 20A, V_{GE} = 15V$	-	65	--	nC
$Q_{ge}$	Gate to Emitter Charge		-	10	--	nC
$Q_{gc}$	Gate to Collector Charge		-	26	--	nC

## Typical Performance Characteristics

Figure 1. Typical Output Characteristics

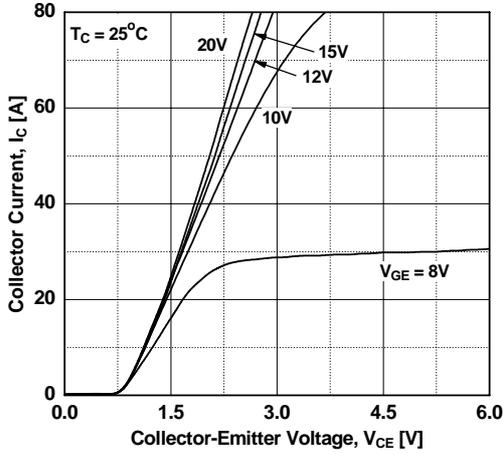


Figure 2. Typical Saturation Voltage Characteristics

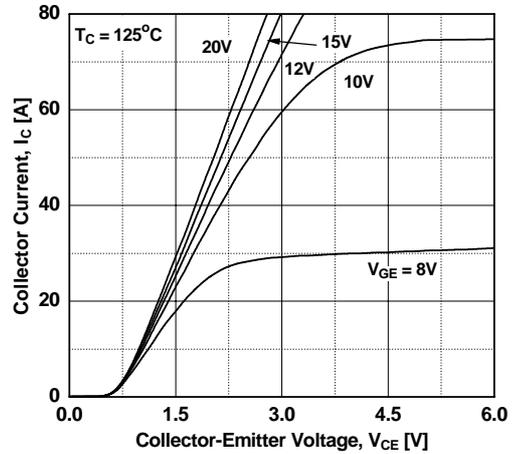


Figure 3. Typical Saturation Voltage Characteristics

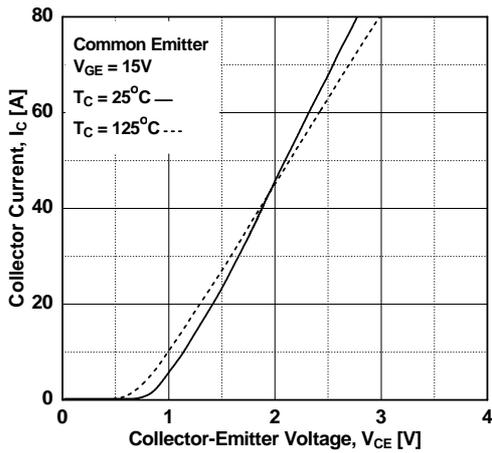


Figure 4. Transfer Characteristics

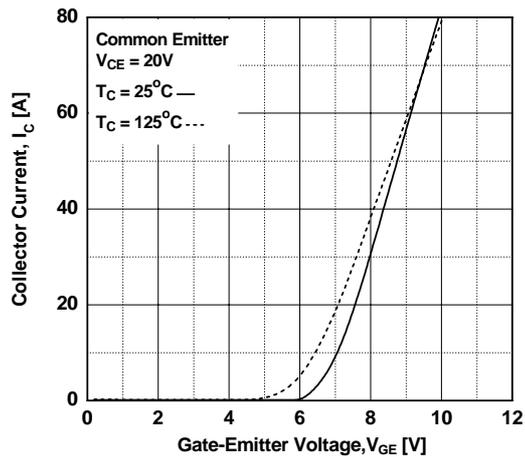


Figure 5. Saturation Voltage vs. Case

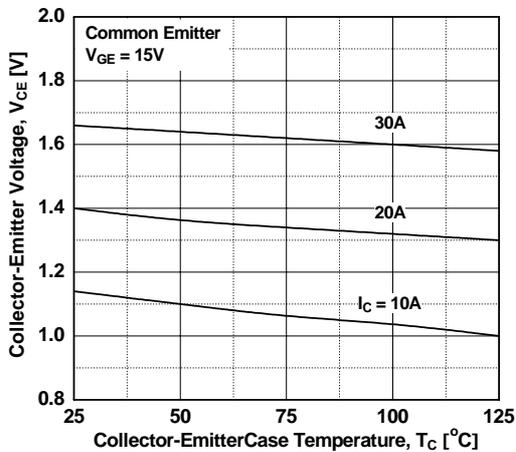
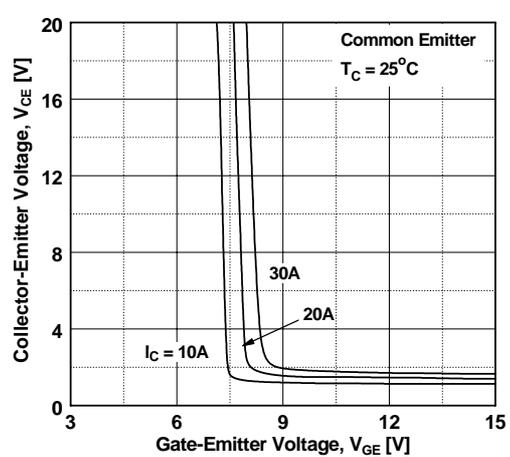
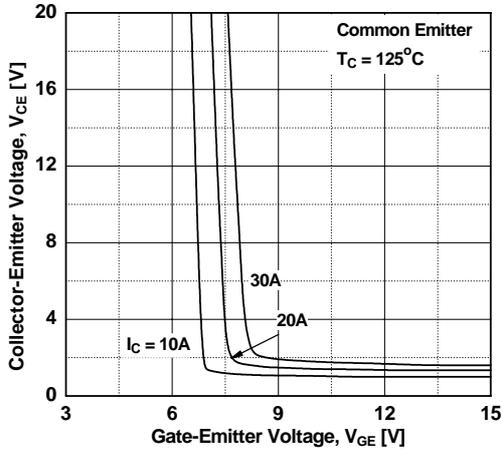


Figure 6. Saturation Voltage vs. Vge

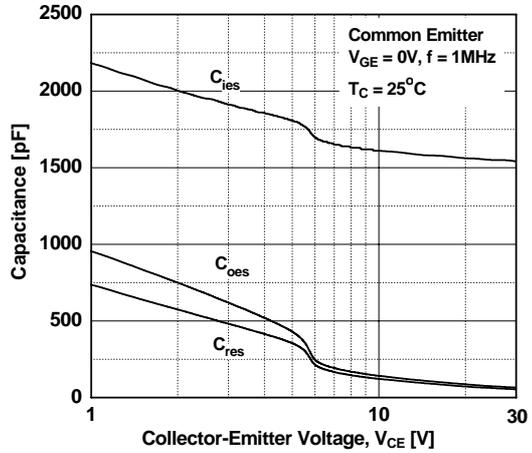


**Typical Performance Characteristics** (Continued)

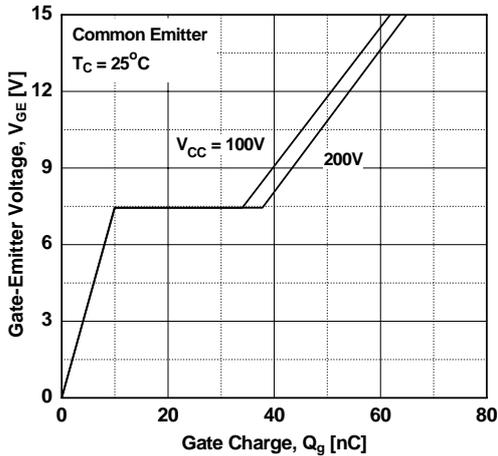
**Figure 7. Saturation Voltage vs. V<sub>GE</sub>**



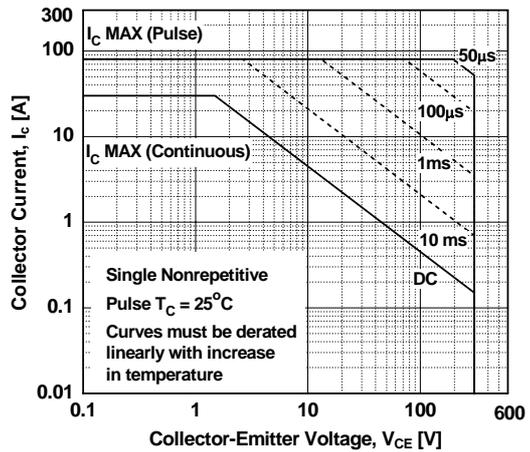
**Figure 8. Capacitance Characteristics**



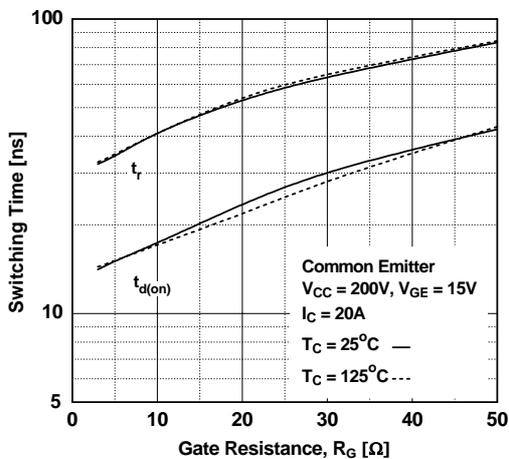
**Figure 9. Gate Charge Characteristics**



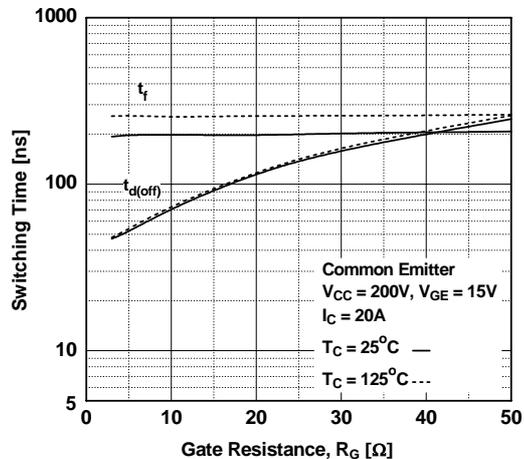
**Figure 10. SOA Characteristics**



**Figure 11. Turn-On Characteristics vs. Gate Resistance**

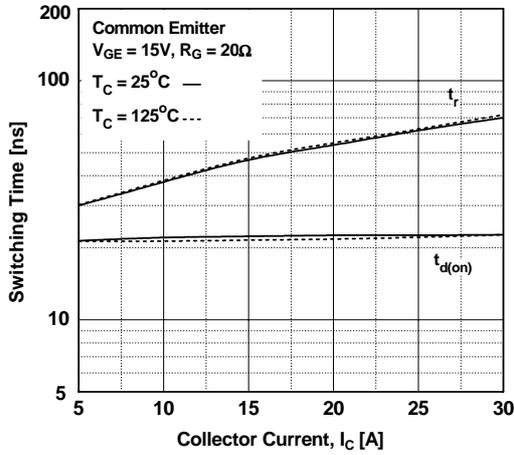


**Figure 12. Turn-Off Characteristics vs. Gate Resistance**

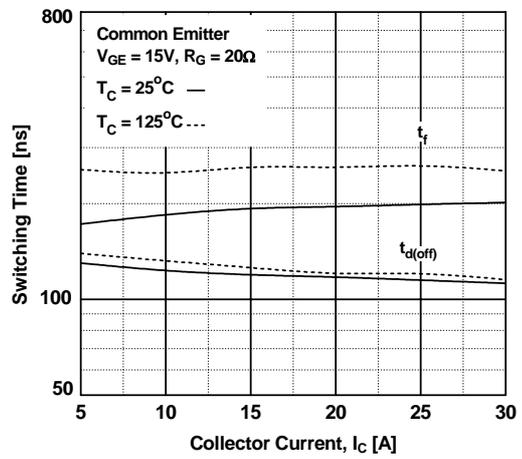


**Typical Performance Characteristics** (Continued)

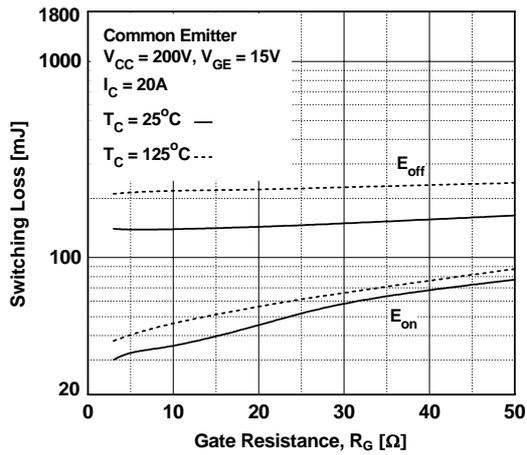
**Figure 13. Turn-On Characteristics vs. Collector Current**



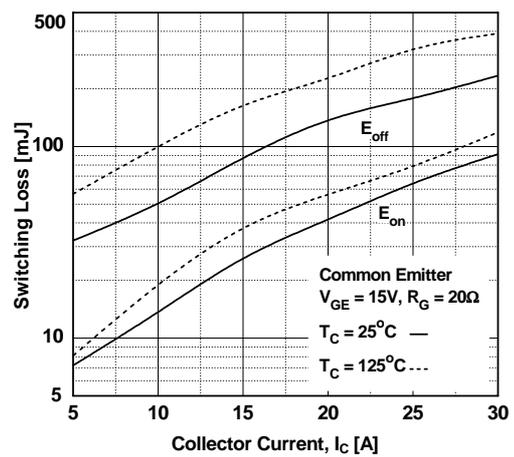
**Figure 14. Turn-Off Characteristics vs. Collector Current**



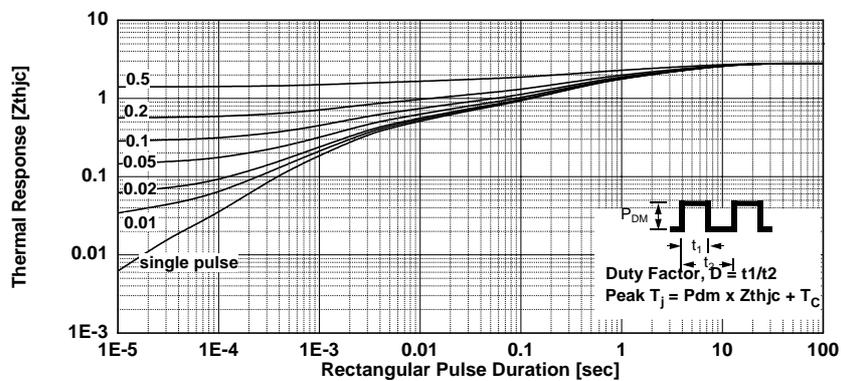
**Figure 15. Switching Loss vs Gate Resistance**



**Figure 16. Switching Loss vs Collector Current**

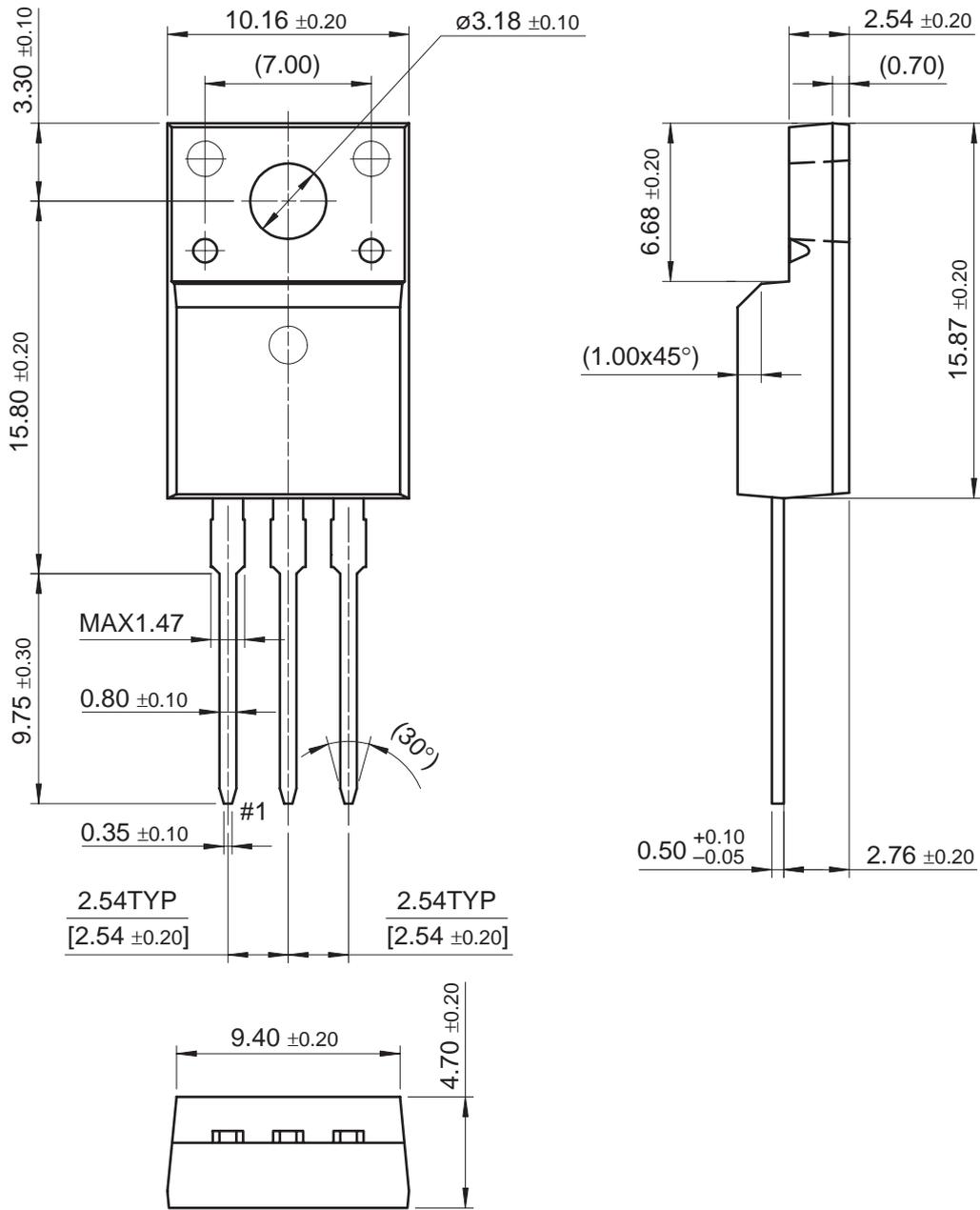


**Figure 18. Transient Thermal Impedance of IGBT**



Mechanical Dimensions

TO-220F



Dimensions in Millimeters



**TRADEMARKS**

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEX®	Green FPS™ e-Series™	POEWEREDGE®	SuperSOT™-8
Build it Now™	GOT™	Power-SPM™	SyncFET™
CorePLUS™	i-Lo™	PowerTrench®	The Power Franchise®
CROSSVOLT™	IntelliMAX™	Programmable Active Droop™	⏻™
CTL™	ISOPLANAR™	QFET®	TinyBoost™
Current Transfer Logic™	MegaBuck™	QS™	TinyBuck™
EcoSPARK®	MICROCOUPLER™	QT Optoelectronics™	TinyLogic®
FACT Quiet Series™	MicroFET™	Quiet Series™	TINYOPTO™
FACT®	MicroPak™	RapidConfigure™	TinyPower™
FAST®	Motion-SPM™	SMART START™	TinyPWM™
FastvCore™	OPTOLOGIC®	SPM®	TinyWire™
FPS™	OPTOPLANAR®	STEALTH™	µSerDes™
FRFET®	PDP-SPM™	SuperFET™	UHC®
Global Power Resource <sup>SM</sup>	Power220®	SuperSOT™-3	UniFET™
Green FPS™	Power247®	SuperSOT™-6	VCX™

**DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

**LIFE SUPPORT POLICY**

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

**PRODUCT STATUS DEFINITIONS**

**Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild Semiconductor. The datasheet is printed for reference information only.

Rev. I29