

# July 2008 Power-SPM<sup>TM</sup>

### FP7G100US60

### **Transfer Molded Type IGBT Module**

#### **General Description**

Fairchild's New IGBT Modules (Transfer Molded Type) provide low conduction and switching losses as well as short circuit ruggedness. They are designed for applications such as Motor control, Uninterrupted Power Supplies (UPS) and general Inverters where short circuit ruggedness is a required feature.

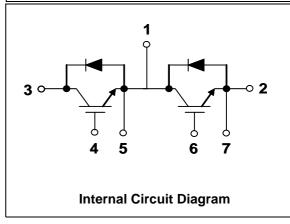
#### **Features**

- Short Circuit rated 10us @Tc=100°C, Vge=15V
- · High Speed Switching
- Low Saturation Voltage : Vce(sat) =2.2V @Ic=100A
- High Input Impedance
- Fast & Soft Anti-Parallel FWD

### **Application**

- Welders
- AC & DC Motor Controls
- General Purpose Inverters
- Robotics
- Servo Controls
- UPS





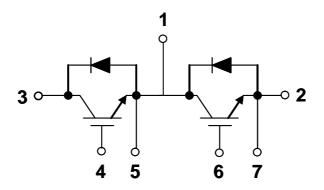
### **Absolute Maximum Ratings**

Symbol	Description		Rating	Units	
V <sub>CES</sub>	Collector-Emitter Voltage		600	V	
V <sub>GES</sub>	Gate-Emitter Voltage		± 20	V	
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 25°C	100	А	
I <sub>CM (1)</sub>	Pulsed Collector Current		200	A	
I <sub>F</sub>	Diode Continuous Forward Current	@ T <sub>C</sub> = 100°C	100	А	
I <sub>FM</sub>	Diode Maximum Forward Current		200	Α	
T <sub>SC</sub>	Short Circuit Withstand Time	@ T <sub>C</sub> = 100°C	10	us	
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	400	W	
P <sub>D</sub>	Operating Junction Temperature		-40 to +125	°C	
T <sub>stg</sub>	Storage Temperature Range		-40 to +125	°C	
V <sub>iso</sub>	Isolation Voltage	@ AC 1minute	2500	V	
Mounting	Power Terminals Screw : M5		2.0	N.m	
Torque	Mounting Screw : M5		2.0	N.m	

## Pin Configuration and Pin Description



**Top View** 



**Internal Circuit Diagram** 

#### **Pin Description**

Pin Number	Pin Description		
1	Emitter of Q1, IGBT, Collector of Q2, IGBT		
2	Emitter of Q2, IGBT		
3	Collector of Q1, IGBT		
4	Gate of Q1, IGBT		
5	Emitter of Q1, IGBT		
6	Gate of Q2, IGBT		
7	Emitter of Q2, IGBT		

### **Electrical Characteristics** (T<sub>J</sub> = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Off Char	racteristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250μA	600	-	-	V
ΔBV <sub>CES</sub> / ΔΤ <sub>J</sub>	Temperature Coeff. of Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 1mA	-	0.6	-	V
I <sub>CES</sub>	Collector Cut-off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	250	uA
I <sub>GES</sub>	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	± 100	nA
On Char	acteristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> =100mA	5.0	6.0	8.5	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 100A, V <sub>GE</sub> = 15V	_	2.2	2.8	V
	1	1		ı	I.	
Dynamic	Characteristics					
C <sub>ies</sub>	Input Capacitance			6085		рF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$ f = 1MHz		725		pF
C <sub>res</sub>	Reverse Capacitance			135		pF
t <sub>d(on)</sub>	Turn-On Delay Time	-	-	34	-	ns
t <sub>d(on)</sub>	Turn-On Delay Time		-	34	-	ns
t <sub>r</sub>	Rise Time		-	24	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 300 \text{ V, } I_{C} = 100 \text{A,}$	-	98	-	ns
t <sub>f</sub>	Fall Time	$R_G = 2.4\Omega$ , $V_{GE} = 15V$ Inductive Load, $T_C = 25^{\circ}C$	-	45	-	ns
E <sub>on</sub>	Turn-On Switching Loss	-	-	0.54	-	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	1.26	-	mJ
E <sub>ts</sub>	Total Switching Loss		-	1.8	-	mJ
t <sub>d(on)</sub>	Turn-On Delay Time		-	33	-	ns
t <sub>r</sub>	Rise Time		-	28	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 300 V, I <sub>C</sub> = 100A,	-	101	-	ns
t <sub>f</sub>	Fall Time	$R_G = 2.4\Omega$ , $V_{GE} = 15V$ Inductive Load, $T_C = 125^{\circ}C$	-	171	-	ns
E <sub>on</sub>	Turn-On Switching Loss		-	1.12	-	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	3.18	-	mJ
E <sub>ts</sub>	Total Switching Loss		_	4.3	-	mJ
T <sub>sc</sub>	Short Circuit Withstand Time	V <sub>CC</sub> = 300 V, V <sub>GE</sub> = 15V @ T <sub>C</sub> = 100°C	10	-	-	us
Q <sub>g</sub>	Total Gate Charge		-	283	-	nC
Q <sub>ge</sub>	Gate-Emitter Charge	$V_{CE} = 300 \text{ V}, I_{C} = 100 \text{A}, V_{GE} = 15 \text{V}$	_	50	-	nC
~ge	Cate Emilion Griange	, C , GE				110

## **Electrical Characteristics of DIODE** ( $T_J = 25$ °C, Unless Otherwise Specified)

Symbol	Parameter	Con	ditions	Min	Тур	Max	Units
$V_{FM}$	Diode Forward Voltage	I <sub>F</sub> = 100A	$T_C = 25^{\circ}C$	-	1.9	2.8	V
			T <sub>C</sub> = 100°C	-	1.8	-	
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 100A di / dt = 200 A/us	$T_C = 25^{\circ}C$	-	85	125	ns
			T <sub>C</sub> = 100°C	-	150	-	
I <sub>rr</sub>	Diode Peak Reverse Recovery Current		T <sub>C</sub> = 25°C	-	8	11	А
			T <sub>C</sub> = 100°C	-	13	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	325	635	nC
			T <sub>C</sub> = 100°C	-	965	-	

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case (IGBT Part, per 1/2 Module)	-	0.25	°C/W
$R_{\theta JC}$	Junction-to-Case (DIODE Part, per 1/2 Module)	-	0.7	°C/W
$R_{\theta CS}$	Case-to-Sink (Conductive grease applied)	0.05	-	°C/W
Weight	Weight of Module	-	90	g

#### **Typical Performance Characteristics**

Fig 1. Typical Output Characteristics

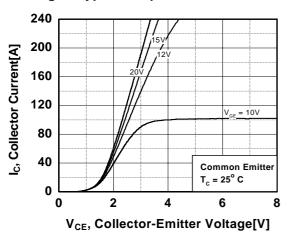


Fig 2. Typical Saturation Voltage Characteristics

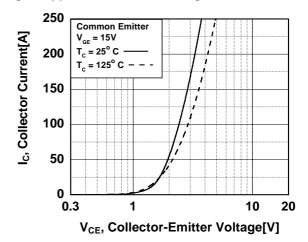


Fig 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

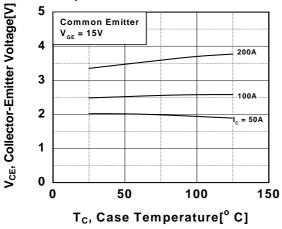


Fig 4. Load Current vs. Frequency

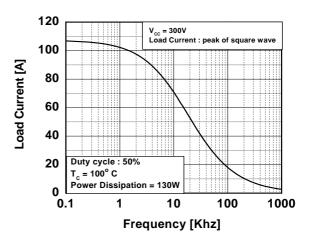


Fig 5. Saturation Voltage vs. V<sub>GF</sub>

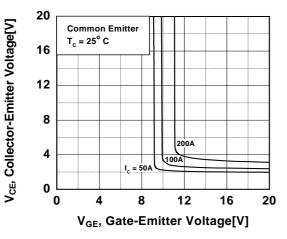
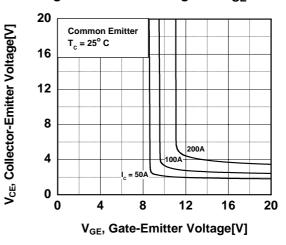


Fig 6. Saturation Voltage vs. V<sub>GF</sub>





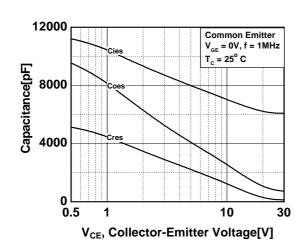


Fig 8. Turn-On Characteristics vs.

Gate Resistance

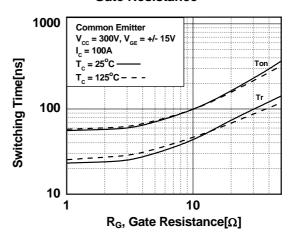


Fig 9. Turn-Off Characteristics vs.
Gate Resistance

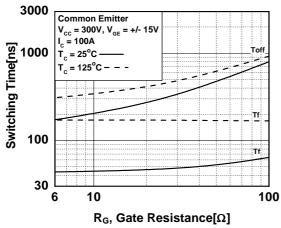


Fig 10. Switching Loss vs. Gate Resistance

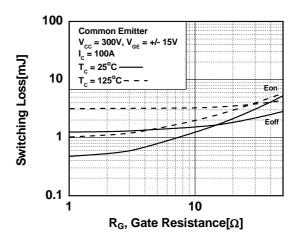


Fig 11. Turn-On Characteristics vs.
Collector Current

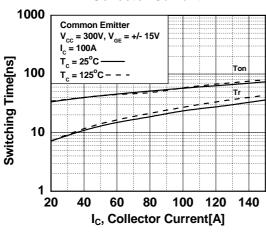
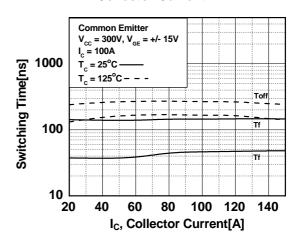


Fig 12. Turn-Off Characteristics vs.
Collector Current





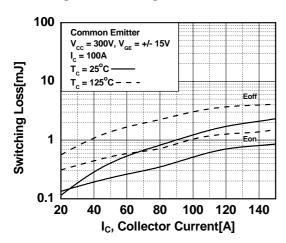


Fig 14. Gate Charge Characteristics

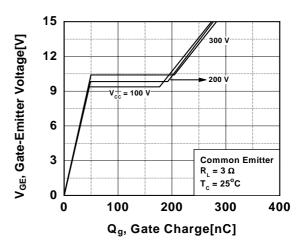


Fig 15. SOA Characteristics

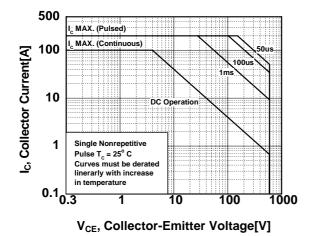


Fig 16. Turn-Off SOA Characteristics

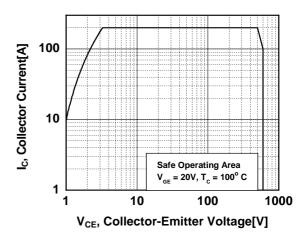


Fig 17. RBSOA Characteristics

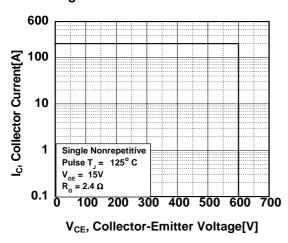


Fig 18. Transient Thermal Impedance

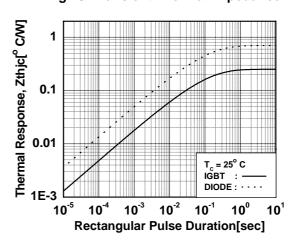


Fig 19. Forward Characteristics

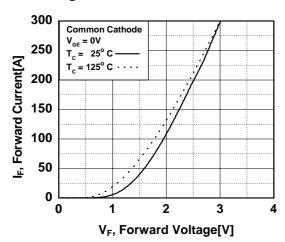
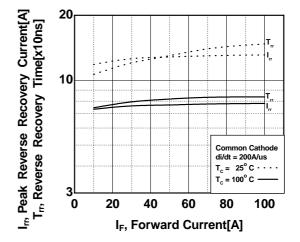
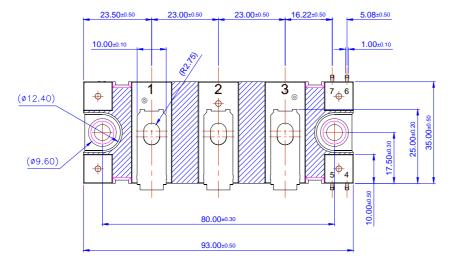
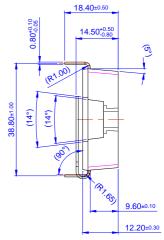


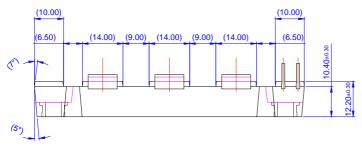
Fig 20. Reverse Recovery Characteristics



### **Detailed Package Outline Drawings**











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