# Digital transistors (built-in resistors) DTB113ZK / DTB113ZS

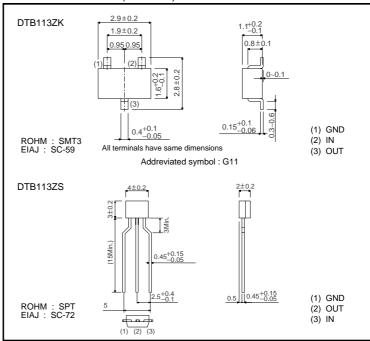
#### Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

## Structure

PNP digital transistor (Built-in resistor type)

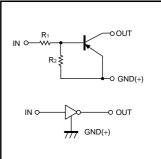
# ●External dimensions (Unit : mm)



## ● Absolute maximum ratings (Ta=25°C)

Darameter	Cymahal	Limits(DT	Unit		
Parameter	Symbol	K	S	Offic	
Supply voltage	Vcc	-50		V	
Input voltage	Vin	-10 t	V		
Output current	lc	-500		mA	
Power dissipation	Pd	200	300	mW	
Junction temperature	Tj	150		೦	
Storage temperature	Tstg	-55 to +150		೮	

# ●Equivalent circuit



# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	_	_	-0.3	V	Vcc=-5V, Io=-100μA
	V <sub>I(on)</sub>	-3	_	_		Vo=-0.3V, Io=-20mA
Output voltage	Vo(on)	-	_	-0.3	V	Io/I:=-50mA/-2.5mA
Input current	lı	-	_	-7.2	mA	V <sub>I</sub> = -5V
Output current	IO(off)	-	_	-0.5	μΑ	Vcc=-50V, Vi=0V
DC current gain	Gı	56	_	_	-	Vo=-5V, Io=-50mA
Input resistance	R <sub>1</sub>	0.7	1	1.3	kΩ	_
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	8	10	12	-	_
Transition frequency	f⊤	-	200	_	MHz	Vc=-10V, I=50mA, f=100MHz *

<sup>\*</sup> Transition frequency of the device

## Packaging specifications

0 0 1							
	Package	SMT3	SPT				
	Packaging type	Taping	Taping				
	Code	T146	TP				
Part No.	Basic ordering unit (pieces)	3000	5000				
DTB113ZK		0	_				
DTB113ZS		1	0				

# •Electrical characteristics curves

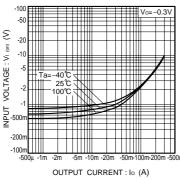


Fig.1 Input voltage vs. output current (ON characteristics)

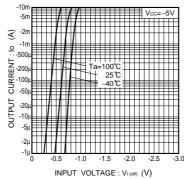


Fig.2 Output current vs. input voltage (OFF characteristics)

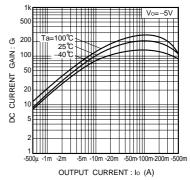


Fig.3 DC current gain vs. output current

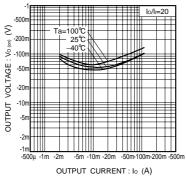


Fig.4 Output voltage vs. output current

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