UNR4221/4222/4223/4224

(UN4221/4222/4223/4224)

Silicon NPN epitaxial planar type

For digital circuits

■ Features

- Costs can be reduced through downsizing of the equipment and reduction of the number of parts
- New S type package, allowing supply with the radial taping

■ Resistance by Part Number

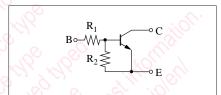
			(\mathbf{R}_1)	(R_2)
•	UNR4221	(UN4221)	$2.2 \text{ k}\Omega$	$2.2~\mathrm{k}\Omega$
•	UNR4222	(UN4222)	$4.7~\mathrm{k}\Omega$	4.7 kΩ
•	UNR4223	(UN4223)	10 kΩ	10 kΩ
•	UNR4224	(UN4224)	$2.2~\mathrm{k}\Omega$	$10 \text{ k}\Omega$

■ Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	50	v
Collector-emitter voltage (Base open)	V _{CEO}	50	V
Collector current	I_{C}	500	mA
Total power dissipation	P_{T}	300	mW
Junction temperature	T_j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Unit: mm 4.0±0.2 0.75 max. 0.45^{+0.20} 0.75 max. 1: Emitter 2: Collector 3: Base NS-B1 Package

Internal Connection



■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)		V_{CBO}	$I_C = 10 \mu A, I_E = 0$	50			V
Collector-emitter voltage (Base open)		V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)		I_{CBO}	$V_{CB} = 50 \text{ V}, I_{E} = 0$			1.0	μΑ
Collector-emitter cutoff current (Base open)		I_{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$			1.0	μΑ
Emitter-base	UNR4221	I_{EBO}	$V_{EB} = 6 \text{ V}, I_{C} = 0$			5.0	mA
cutoff current	UNR4222		Sign 1			2.0	
(Collector open)	UNR4223/4224					1.0	
Forward current	UNR4221	h_{FE}	$V_{CE} = 10 \text{ V}, I_{C} = 100 \text{ mA}$	40			_
transfer ratio	UNR4222			50			
	UNR4223/4224			60			
Collector-emitter saturation voltage		V _{CE(sat)}	$I_{\rm C} = 100 \text{ mA}, I_{\rm B} = 5 \text{ mA}$			0.25	V
Output voltage high-level		V_{OH}	$V_{CC} = 5 \text{ V}, V_B = 0.5 \text{ V}, R_L = 500 \Omega$	4.9			V
Output voltage low-level		V_{OL}	$V_{CC} = 5 \text{ V}, V_{B} = 3.5 \text{ V}, R_{L} = 500 \Omega$			0.2	V

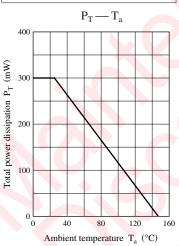
Note) The part numbers in the parenthesis show conventional part number.

■ Electrical Characteristics (continued) $T_a = 25$ °C ± 3 °C

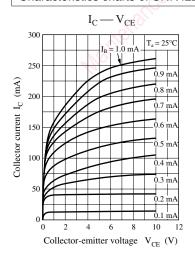
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Transition frequency		f_T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Input resistance	UNR4221/4224	R_1		-30%	2.2	+30%	kΩ
	UNR4222				4.7		
	UNR4223				10		
Resistance ratio		R_1/R_2		0.8	1.0	1.2	
	UNR4224			0.17	0.22	0.27	

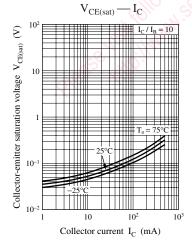
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

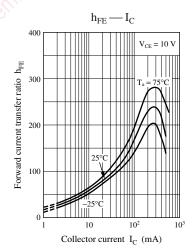
Common characteristics chart



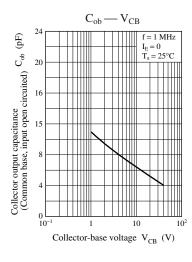
Characteristics charts of UNR4221

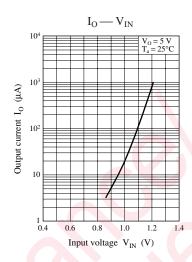


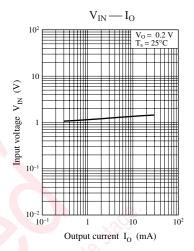




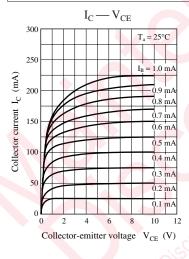
Panasonic

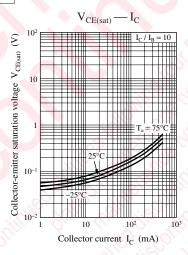


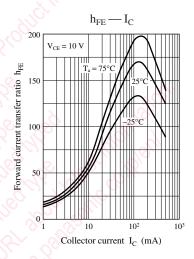


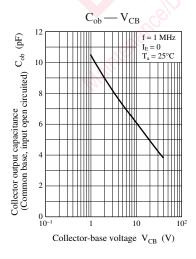


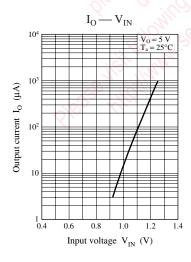
Characteristics charts of UNR4222

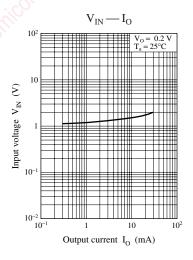




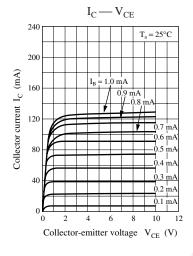


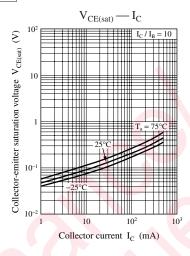


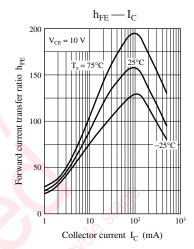


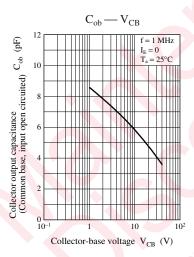


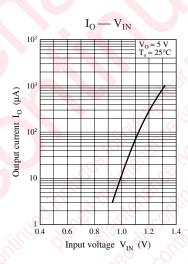
Characteristics charts of UNR4223

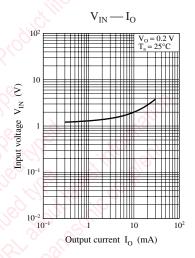




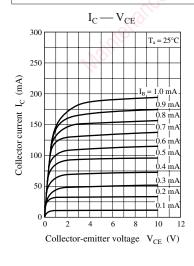


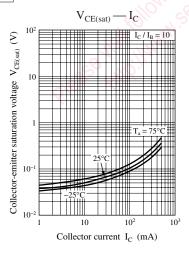


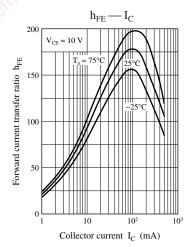


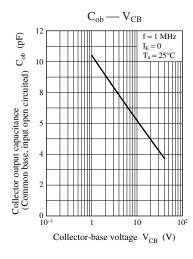


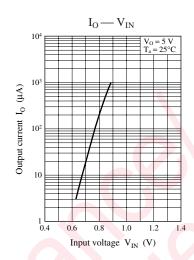
Characteristics charts of UNR4224

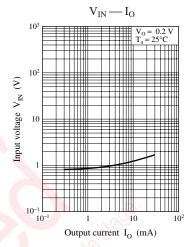












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