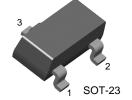


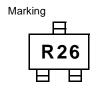
FJV3106R

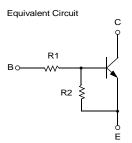
Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R₁=10K Ω , R₂=47K Ω)
- Complement to FJV4106R



1. Base 2. Emitter 3. Collector





NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	10	V
I _C	Collector Current	100	mA
P _C	Collector Power Dissipation	200	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =10μA, I _E =0	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C=100\mu A, I_B=0$	50			V
I _{CBO}	Collector Cut-off Current	V_{CB} =40V, I_{E} =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	68			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
C _{ob}	Output Capacitance	V _{CE} =10mA, I _E =0 f=1.0MHz		3.7		pF
f _T	Current Gain Bandwidth Product	V _{CB} =10V, I _C =5mA		250		MHz
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.3			V
V _I (on)	Input On Voltage	V_{CE} =0.3V, I_{C} =1mA			1.4	V
R ₁	Input Resistor		7	10	13	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.19	0.21	0.24	

Typical Characteristics

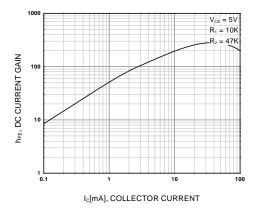


Figure 1. DC current Gain

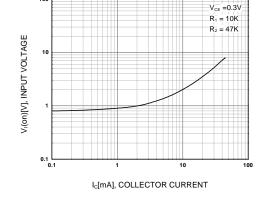


Figure 2. Input On Voltage

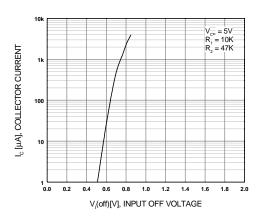


Figure 3. Input Off Voltage

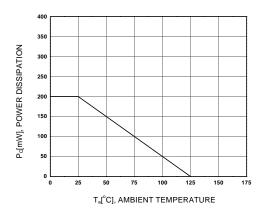
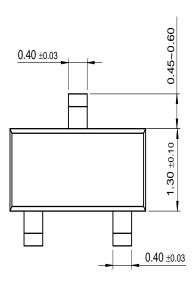


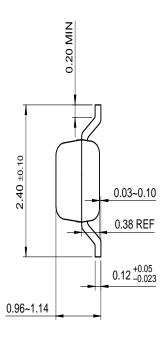
Figure 4. Power Derating

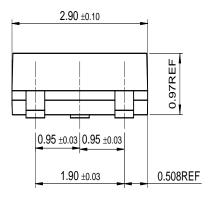
Rev. A, July 2002

Package Dimensions

SOT-23







Dimensions in Millimeters

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lFET™	FASTr™	MicroFET™	PowerTrench®	SuperSOT™-6
OSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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