

4V Drive Nch MOSFET

RSF015N06

Structure

Silicon N-channel MOSFET

Features

- 1) Built-in G-S Protection Diode.
- 2) Small Surface Mount Package (TUMT3).
- 3) Low voltage drive. (4V)

Application

Switching

Packaging specifications

	Package	Taping	
Type	Code	TL	
	Basic ordering unit (pieces)	3000	
RSF015N0	0		

● Absolute maximum ratings (Ta = 25°C)

Param	Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	60	V
Gate-source voltage		V _{GSS} *1	±20	V
Drain current	Continuous	I _D *1	±1.5	Α
	Pulsed	I _{DP} *1	±6.0	Α
Source current (Body Diode)	Continuous	I _S	0.6	Α
	Pulsed	I _{SP} *1	6.0	Α
Power dissipation		P _D *2	0.8	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

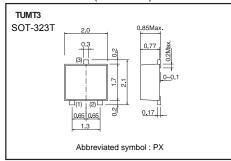
^{*1} Pw≤10µs, Duty cycle≤1%

• Thermal resistance

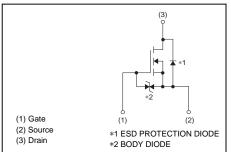
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	156	°C/W

^{*}Mounted on a ceramic board.

● Dimensions (Unit : mm)



• Inner circuit



^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}		-	±10	μA	$V_{GS}=\pm20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	1	-	>	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	1	μA	V _{DS} =60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	1	2.5	٧	V_{DS} =10V, I_{D} =1mA
Static ducin accuracy on state		1	210	290		I _D =1.5A, V _{GS} =10V
Static drain-source on-state resistance	R _{DS (on)}	1	240	330	mΩ	I _D =1.5A, V _{GS} =4.5V
		1	255	350		I _D =1.5A, V _{GS} =4.0V
Forward transfer admittance	I Y _{fs} I*	1.0	-	-	S	I _D =1.5A, V _{DS} =10V
Input capacitance	C _{iss}	-	110	-	pF	V _{DS} =10V
Output capacitance	C _{oss}	1	28	-	рF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	12	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	6	-	ns	I _D =0.7A, V _{DD} ≒30V
Rise time	t _r *	1	9	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	1	15	-	ns	$R_L=42.8\Omega$
Fall time	t _f *	1	10	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	1	2.0	-	nC	I _D =1.5A
Gate-source charge	Q _{gs} *	-	0.8	-	nC	V _{DD} ≒30V
Gate-drain charge	Q _{gd} *	-	0.5	-	nC	V _{GS} =5V

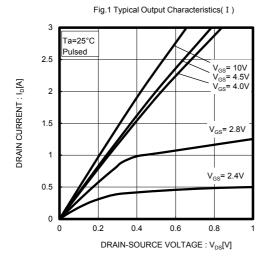
^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.2	V	I _s =1.5A, V _{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)





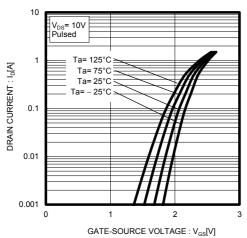


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

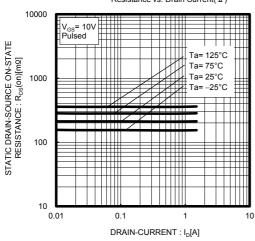


Fig.2 Typical Output Characteristics(II)

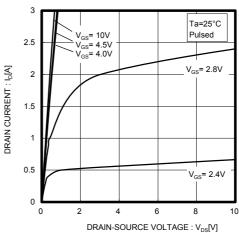


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

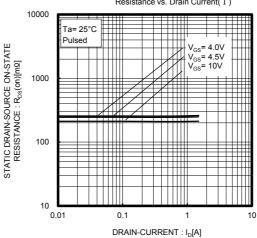
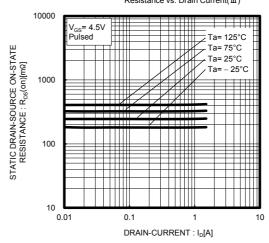
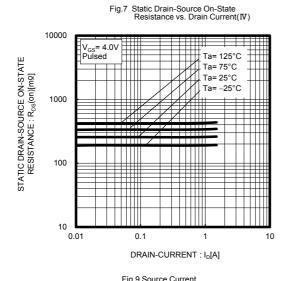
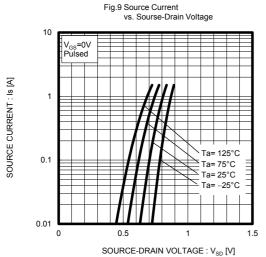
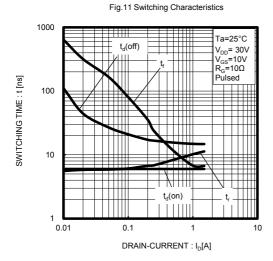


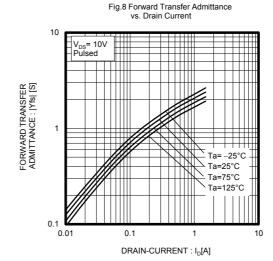
Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(Ⅲ)

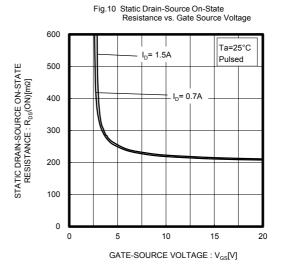


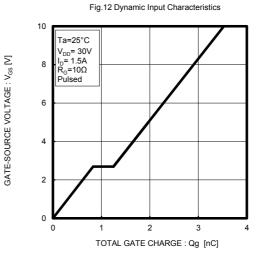


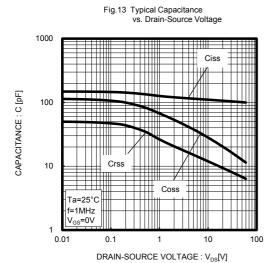












Measurement circuits

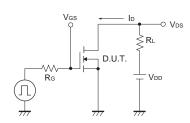


Fig.1-1 Switching Time Measurement Circuit

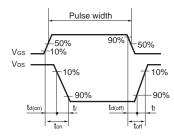


Fig.1-2 Switching Waveforms

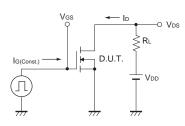


Fig.2-1 Gate Charge Measurement Circuit

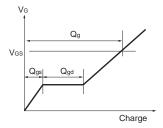


Fig.2-2 Gate Charge Waveform

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