# 10V Drive Nch MOS FET RDX080N50

### Structure

Silicon N-channel MOS FET

# ● Features

- 1) Low on-resistance.
- 2) Low input capacitance.
- 3) Excellent resistance to damage from static electricity.

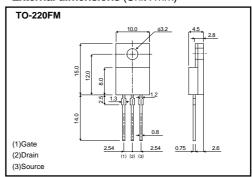
# Applications

Switching

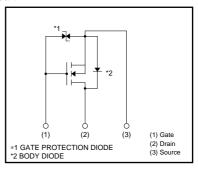
### Packaging specifications

	Package	Bulk
Type	Code	_
	Basic ordering unit (pieces)	500
RDX080N50		0

# ●External dimensions (Unit:mm)



### •Inner circuit



# ●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit	
Drain-source voltage		$V_{DSS}$		500	V	
Gate-source voltage		V <sub>GSS</sub>		±30	V	
Drain current	Continuous	$I_D$	*1	±8	Α	
Drain current	Pulsed	$I_{DP}$	*2	±32	Α	
Source current (Body diode)	Continuous	Is		8	Α	
	Pulsed	I <sub>SP</sub>	*2	32	Α	
Avalanche current		I <sub>AS</sub>	*3	8	Α	
Avalanche energy		Eas	*4	85	mJ	
Total power dissipation (Tc=25°C)		PD		40	W	
Channel temperature		Tch		150	°C	
Range of storage temperature		Tstg		-55 to +150	°C	

# Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	Rth(ch-c)	3.125	°C/W

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	V <sub>GS</sub> = ±25V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	500	_	_	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	_	25	μΑ	V <sub>DS</sub> = 500V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	2.0	_	4.0	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	-	0.65	0.85	Ω	I <sub>D</sub> = 4A, V <sub>G</sub> s= 10V
Forward transfer admittance	Y <sub>fs</sub>   *	3	5	_	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A
Input capacitance	Ciss	-	920	_	pF	V <sub>DS</sub> = 25V
Output capacitance	Coss	-	125	_	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	-	27	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	-	20	_	ns	Vpp≒ 150V
Rise time	tr *	-	22	_	ns	ID= 4A VGS= 10V
Turn-off delay time	t <sub>d (off)</sub> *	-	55	_	ns	VGS= 10V RL= 37.5Ω
Fall time	t <sub>f</sub> *	-	30	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	-	28	_	nC	Vpp≒250V, Vgs=10V
Gate-source charge	Q <sub>gs</sub> *	-	6.5	_	nC	I <sub>D</sub> = 8A
Gate-drain charge	Q <sub>gd</sub> *	_	12	_	nC	$R_L=31.3\Omega$ , $R_G=10\Omega$

\*Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	1.5	V	I <sub>S</sub> = 8A, V <sub>GS</sub> =0V
Reverse recovery time	trr	_	375	_	ns	I <sub>DR</sub> = 8A, V <sub>GS</sub> =0V
Reverse recovery charge	Qrr	_	2.5	_	μC	di/dt= 100A / μs

<sup>\*</sup> Pulsed

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