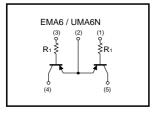
Emitter common (dual digital transistors) EMA6 / UMA6N

● Feature

1) Two DTA144T chips in a EMT or UMT package.

●Equivalent circuit



● Package, marking, and packaging specifications

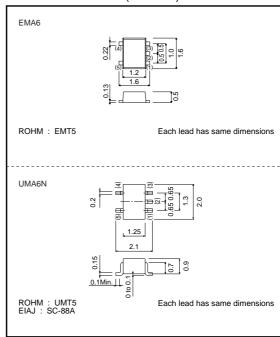
Туре	EMA6	UMA6N	
Package	EMT5	UMT5	
Marking	A6	A6	
Code	T2R	TR	
Basic ordering unit (pieces)	8000	3000	

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	-50	V	
Collector-emitter voltage	Vceo	-50	V	
Emitter-base voltage	Vebo	-5	V	
Collector current	lc	-100	mA	
Collector power dissipation	Pc	150(TOTAL)	mW *1	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded.

●External dimensions (Unit : mm)



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic= -50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	BVEBO	-5	-	_	V	Iε= -50μA
Collector cutoff current	Ісво	-	-	-0.5	μΑ	Vcb= -50V
Emitter cutoff current	ІЕВО	-	-	-0.5	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VcE(sat)	-	-	-0.3	V	Ic/I _B = -5mA/ -0.5mA
DC current transfer ratio	hre	100	250	600	_	Vce/Ic= -5V/ -1mA
Transition frequency	fτ	-	250	-	MHz	V _{EB} = -10V, I _E =5mA, f=100MHz *
Input resistance	R ₁	32.9	47	61.1	kΩ	-

^{*}Transition frequency of the device.

•Electrical characteristics curves

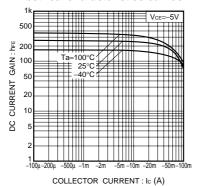


Fig.1 DC current gain vs.collector current

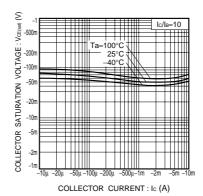


Fig.2 Collector-emitter saturation voltage vs.collector current

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