

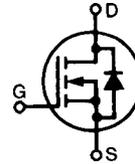
# HiPerFET™ Power MOSFETs

**IXFH/IXFT 68N20**  
**IXFH/IXFT 74N20**

$V_{DSS}$	$I_{D25}$	$R_{DS(on)}$
200 V	68 A	35 mΩ
200 V	74 A	30 mΩ

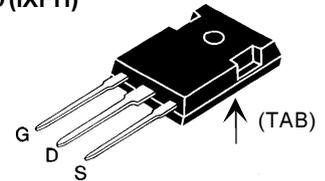
$t_{rr} \leq 200$  ns

N-Channel Enhancement Mode  
High dv/dt, Low  $t_{rr}$ , HDMOS™ Family

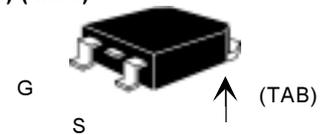


Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ\text{C}$ to $150^\circ\text{C}$	200	V
$V_{DGR}$	$T_J = 25^\circ\text{C}$ to $150^\circ\text{C}$ ; $R_{GS} = 1$ MΩ	200	V
$V_{GS}$	Continuous	±20	V
$V_{GSM}$	Transient	±30	V
$I_{D25}$	$T_C = 25^\circ\text{C}$	68N20	68 A
		74N20	74 A
$I_{DM}$	$T_C = 25^\circ\text{C}$ , pulse width limited by $T_{JM}$	68N20	272 A
		74N20	296 A
$I_{AR}$	$T_C = 25^\circ\text{C}$	68N20	68 A
		74N20	74 A
$E_{AR}$	$T_C = 25^\circ\text{C}$	45	mJ
dv/dt	$I_S \leq I_{DM}$ , $di/dt \leq 100$ A/μs, $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ\text{C}$ , $R_G = 2$ Ω	5	V/ns
$P_D$	$T_C = 25^\circ\text{C}$	360	W
$T_J$		-55 ... +150	°C
$T_{JM}$		150	°C
$T_{stg}$		-55 ... +150	°C
$T_L$	1.6 mm (0.062 in.) from case for 10 s	300	°C
$M_d$	Mounting torque	1.13/10	Nm/lb.in.
Weight		6	g

TO-247 AD (IXFH)



TO-268 (D3) (IXFT)



G = Gate, D = Drain,  
S = Source, TAB = Drain

## Features

- International standard packages
- Low  $R_{DS(on)}$  HDMOS™ process
- Rugged polysilicon gate cell structure
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
- easy to drive and to protect
- Fast intrinsic Rectifier

## Applications

- DC-DC converters
- Synchronous rectification
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- AC motor control
- Temperature and lighting controls
- Low voltage relays

## Advantages

- Easy to mount with 1 screw (TO-247) (isolated mounting screw hole)
- High power surface package
- High power density

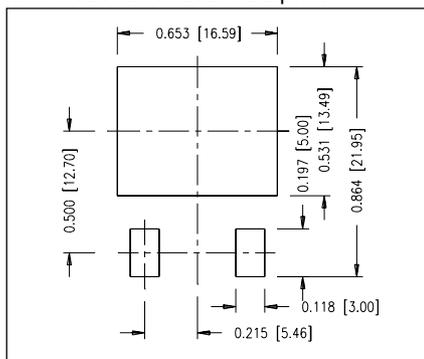
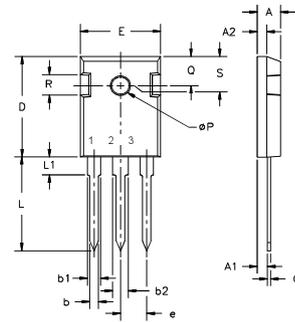
Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		min.	typ.	max.
$V_{DSS}$	$V_{GS} = 0$ V, $I_D = 1$ mA	200		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 4$ mA	2		V
$I_{GSS}$	$V_{GS} = \pm 20$ V <sub>DC</sub> , $V_{DS} = 0$			±100 nA
$I_{DSS}$	$V_{DS} = 0.8 \cdot V_{DSS}$ $V_{GS} = 0$ V	$T_J = 25^\circ\text{C}$		200 μA
		$T_J = 125^\circ\text{C}$		1 mA
$R_{DS(on)}$	$V_{GS} = 10$ V, $I_D = 0.5 I_{D25}$  Pulse test, $t \leq 300$ μs, duty cycle $d \leq 2$ %	74N20		30 mΩ
		68N20		35 mΩ

Symbol	Test Conditions	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 10\text{ V}; I_D = 0.5 I_{D25}$ , pulse test	35	45	S
$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$		5400	pF
$C_{oss}$			1160	pF
$C_{rss}$			560	pF
$t_{d(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$ $R_G = 2\ \Omega$ (External)		40	ns
$t_r$			55	ns
$t_{d(off)}$			120	ns
$t_f$			26	ns
$Q_{g(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$		280	nC
$Q_{gs}$			39	nC
$Q_{gd}$			135	nC
$R_{thJC}$	(TO-247 Package)		0.35	KW
$R_{thCK}$				KW

IXFH68N20 & IXFH74N80 characteristic curves can be found in the IXFK72N20/IXFK80N20 data sheet.

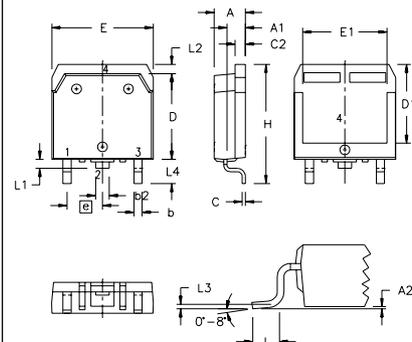
**Source-Drain Diode**

Symbol	Test Conditions	Characteristic Values			
		Min.	Typ.	Max.	
$I_s$	$V_{GS} = 0\text{ V}$	68N20 74N20		68 74	A A
$I_{SM}$	Repetitive; pulse width limited by $T_{JM}$	68N20 74N20		272 296	A A
$V_{SD}$	$I_F = I_s, V_{GS} = 0\text{ V}$ , Pulse test, $t \leq 300\ \mu\text{s}$ , duty cycle $d \leq 2\%$			1.5	V
$t_{rr}$	$I_F = 25\text{ A}$ $-di/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 100\text{ V}$			200	ns
$Q_{RM}$			0.85		$\mu\text{C}$
$I_{RM}$			8		A

**Min Recommended Footprint**

**TO-247 AD (IXFH) Outline**


Terminals:  
1 - Gate  
2 - Drain  
3 - Source  
Tab - Drain

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.7	5.3	.185	.209
A <sub>1</sub>	2.2	2.54	.087	.102
A <sub>2</sub>	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b <sub>1</sub>	1.65	2.13	.065	.084
b <sub>2</sub>	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	0.205	0.225
L	19.81	20.32	.780	.800
L <sub>1</sub>		4.50		.177
∅P	3.55	3.65	.140	.144
Q	5.89	6.40	0.232	0.252
R	4.32	5.49	.170	.216
S	6.15	BSC	.242	BSC

**TO-268 Outline**


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.193	.201	4.90	5.10
A1	.106	.114	2.70	2.90
A2	.001	.010	0.02	0.25
b	.045	.057	1.15	1.45
b2	.075	.083	1.90	2.10
C	.016	.026	0.40	0.65
C2	.057	.063	1.45	1.60
D	.543	.551	13.80	14.00
D1	.488	.500	12.40	12.70
E	.624	.632	15.85	16.05
E1	.524	.535	13.30	13.60
e		.215 BSC		5.45 BSC
H	.736	.752	18.70	19.10
L	.094	.106	2.40	2.70
L1	.047	.055	1.20	1.40
L2	.039	.045	1.00	1.15
L3		.010 BSC		0.25 BSC
L4	.150	.161	3.80	4.10

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETS and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,881,106 5,017,508 5,049,961 5,187,117 5,486,715  
4,850,072 4,931,844 5,034,796 5,063,307 5,237,481 5,381,025