

## 700V N-Channel Power MOSFET



**ITO-220** 

#### Pin Definition:

- 1. Gate
- 2. Drain3. Source

# PRODUCT SUMMARY



## **General Description**

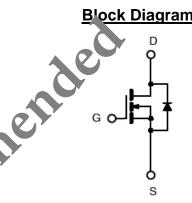
The TSM8N70 N-Channel enhancement mode Power MOSFET is produced by planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

#### **Features**

- Low  $R_{DS(ON)} 0.75\Omega$  (Typ.)
- Low gate charge typical @ 32nC (Typ.)
- Low Crss typical @ 13.7pF (Typ.)
- Fast Switching

#### **Ordering Information**

| Part No.      | Package | Packing      |  |  |
|---------------|---------|--------------|--|--|
| TSM8N70CI C0  | ITO-220 | 50pcs / Tube |  |  |
| TSM8N70CI C0G | ITO-220 | 50pcs / Tube |  |  |



N-Channel MOSFET

## Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

| Parameter                                       | Symbol           | Limit       | Unit |
|---|------------------|-------------|------|
| Drain-Source Voltage                            | $V_{DS}$         | 700         | V    |
| Gate-Source Voltage                             | $V_{GS}$         | ±30         | V    |
| Tc = 25°C                                       | - I <sub>D</sub> | 8           | Α    |
| Continuous Drain Current Tc = 100°C             |                  | 4.8         | Α    |
| Pulsed Drain Current *                          | I <sub>DM</sub>  | 32          | Α    |
| Single Pulse Avalanche Energy (Note 2)          | E <sub>AS</sub>  | 266         | mJ   |
| Avalanche Current (Repetitive) (Note 2          | I <sub>AS</sub>  | 8           | Α    |
| Single Pulse Avalanche Energy (Note 1)          | E <sub>AR</sub>  | 11.6        | mJ   |
| Avalanche Current (Repetitive) (Note 1)         | I <sub>AR</sub>  | 8           | Α    |
| Total Power Dissipation @ T <sub>C</sub> = 25°C | P <sub>TOT</sub> | 40          | W    |
| Operating Junction Temperature                  | TJ               | 150         | °C   |
| Storage Temperature Range                       | T <sub>STG</sub> | -55 to +150 | °C   |

Note: Limited by maximum junction temperature

#### **Thermal Performance**

| Parameter                                | Symbol           | Limit | Unit |  |
|--|------------------|-------|------|--|
| Thermal Resistance - Junction to Case    | R⊖ <sub>JC</sub> | 3.1   | °C/W |  |
| Thermal Resistance - Junction to Ambient | $R\Theta_{JA}$   | 62.5  | °C/W |  |

Notes: Surface mounted on FR4 board t ≤ 10sec



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Electrical Specifications (Ta = 25°C unless otherwise noted)

|                                   | Conditions                       | Symbol              | Min | Тур  | Max | Unit |
|-----------------------------------|----------------------------------|---------------------|-----|------|-----|------|
| Static                            |                                  |                     |     |      |     |      |
| Drain-Source Breakdown Voltage    | $V_{GS} = 0V, I_D = 250uA$       | $BV_{DSS}$          | 700 |      |     | V    |
| Drain-Source On-State Resistance  | $V_{GS} = 10V, I_D = 4A$         | R <sub>DS(ON)</sub> |     | 0.75 | 0.9 | Ω    |
| Gate Threshold Voltage            | $V_{DS} = V_{GS}, I_{D} = 250uA$ | $V_{GS(TH)}$        | 2.0 |      | 4.0 | V    |
| Zero Gate Voltage Drain Current   | $V_{DS} = 700V, V_{GS} = 0V$     | I <sub>DSS</sub>    |     |      | 1   | uA   |
| Gate Body Leakage                 | $V_{GS} = \pm 30V, V_{DS} = 0V$  | $I_{GSS}$           |     |      | ±10 | uA   |
| Forward Transfer Conductance      | $V_{DS} = 10V, I_{D} = 4A$       | g <sub>fs</sub>     |     | 11   |     | S    |
| Dynamic                           |                                  |                     |     |      |     |      |
| Total Gate Charge                 | \/ FCO\/   OA                    | $Q_g$               |     | 32   |     |      |
| Gate-Source Charge                | $V_{DS} = 560V, I_{D} = 8A,$     | $Q_{gs}$            | -   | 9    |     | nC   |
| Gate-Drain Charge                 | $V_{GS} = 10V$                   | $Q_gd$              | -1  | 8    |     |      |
| Input Capacitance                 | N/ 05N/ N/ 0N/                   | C <sub>iss</sub>    | 2   | 2006 |     |      |
| Output Capacitance                | $V_{DS} = 25V, V_{GS} = 0V,$     | Coss                |     | 148  |     | pF   |
| Reverse Transfer Capacitance      | f = 1.0MHz                       | rss                 | 1   | 13.7 |     |      |
| Switching                         |                                  |                     |     |      |     |      |
| Turn-On Delay Time                |                                  | t <sub>d(on)</sub>  |     | 23   |     |      |
| Turn-On Rise Time                 | $V_{GS} = 10V, I_D = 10A$        | t <sub>r</sub>      |     | 69   |     | ~0   |
| Turn-Off Delay Time               | $V_{DD} = 300V, R_G = 2^{-1}Q$   | $t_{d(off)}$        |     | 144  |     | nS   |
| Turn-Off Fall Time                |                                  | t <sub>f</sub>      |     | 77   |     |      |
| Source-Drain Diode Ratings and Ch | aracteristic                     |                     |     |      |     |      |
| Source Current                    | Integrativeverse diode in        | Is                  | 1   | 1    | 8   | Α    |
| Source Current (Pulse)            | Te MOSFET                        | I <sub>SM</sub>     | 1   | 1    | 32  | Α    |
| Diode Forward Voltage             | $I_S = 3A$ , $V_{GS} = 0V$       | $V_{SD}$            | -   |      | 1.4 | V    |
| Reverse Recovery Time             | $V_{GS} = 0V, I_S = 8A,$         | t <sub>fr</sub>     | -   | 420  |     | nS   |
| Reverse Recovery Charge           | $dI_F/dt = 100A/us$              | $Q_{fr}$            | 1   | 4.2  |     | uC   |

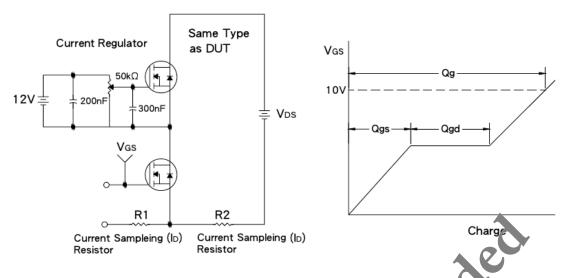
Note 1: Repetitive Rating: Pulse width Limited by Maximum Junction Temperature Note 2:  $V_{DD} = 50V$ ,  $I_{AS} = 8A$ , L = 7.74mH,  $R_{G} = 25\Omega$ , Starting  $T_{J} = 25^{\circ}C$  Note 3: Pulse test: pulse width  $\leq 300$ uS, duty cycle  $\leq 2\%$  Note 4: Essentially Independent of Operating Temperature



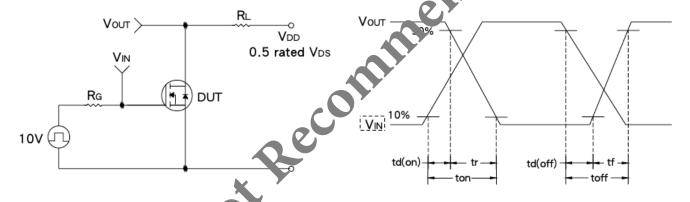
# 700V N-Channel Power MOSFET



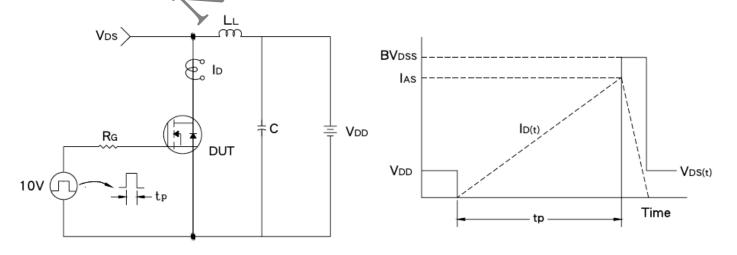
## **Gate Charge Test Circuit & Waveform**



## **Resistive Switching Test Circuit & Waveform**



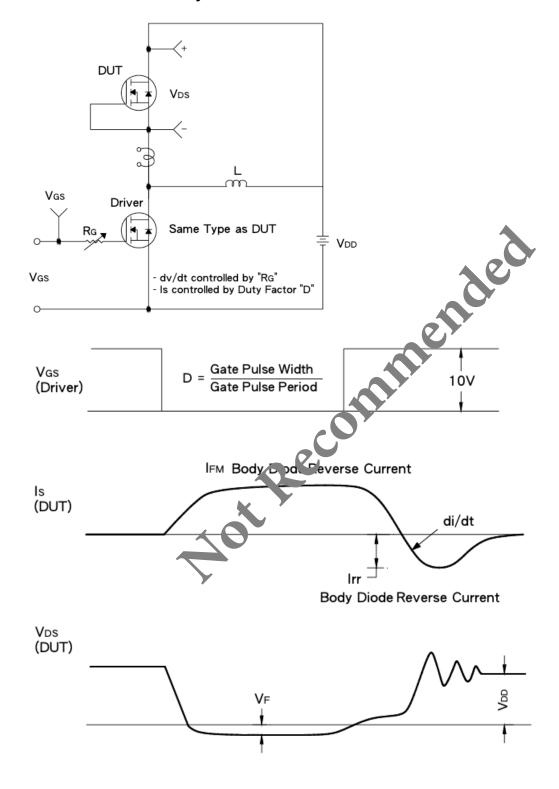
# E<sub>AS</sub> Test Circuit & Waveform





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## **Diode Reverse Recovery Time Test Circuit & Waveform**







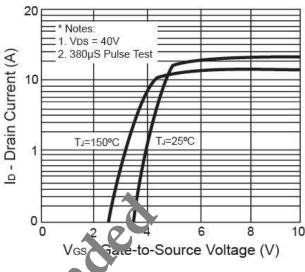
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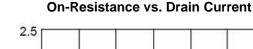


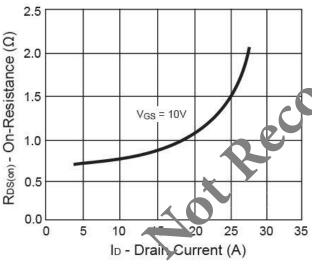
#### Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

#### **Output Characteristics** 20 \* Notes: 1. 380µS Pulse Test 2. TJ=25°C 16 Ib - Drain Current (A) V<sub>GS</sub> = 7~5.5V 12 8 5V 4.5V 0 20 8 12 4 16 0 V<sub>DS</sub> - Drain-to-Source Voltage (V)

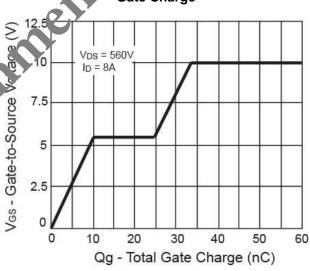
#### **Transfer Characteristics**



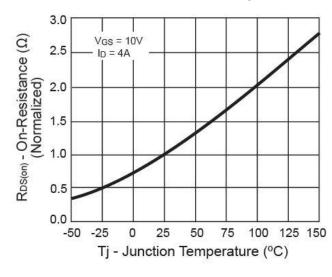




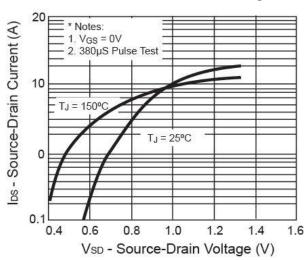
**Gate Charge** 



### On-Resistance vs. Junction Temperature



#### Source-Drain Diode Forward Voltage

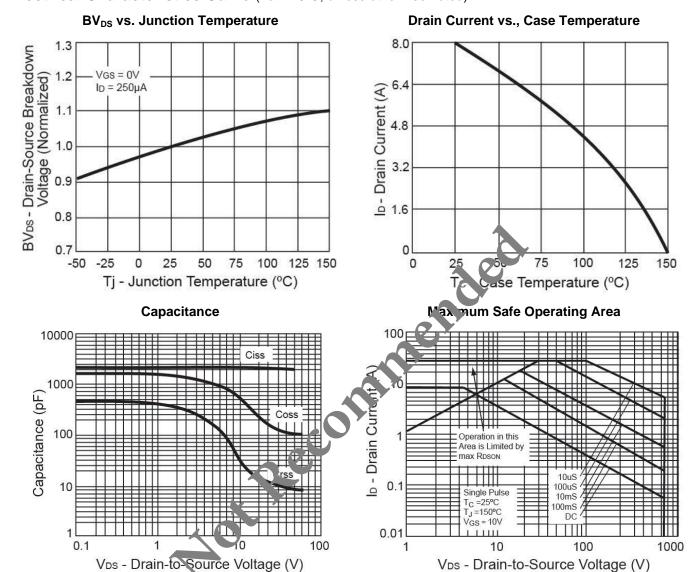




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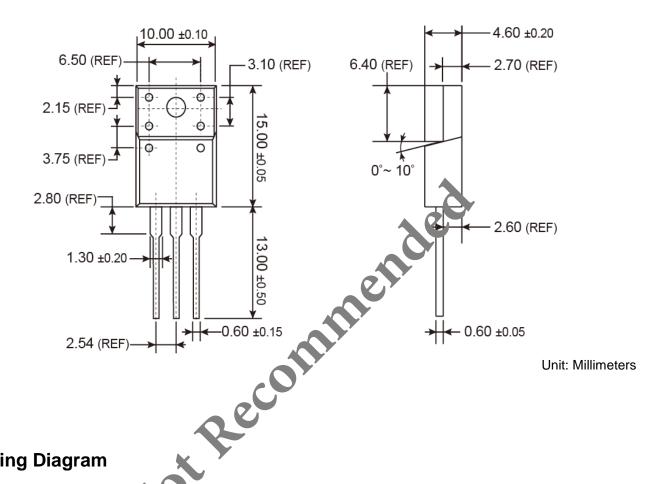
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Version: D1707

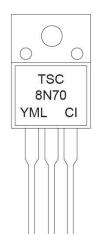




# **ITO-220 Mechanical Drawing**



# **Marking Diagram**



ear Code

= Month Code

(A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)

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= Lot Code

Version: D1707

# **TSM8N70**700V N-Channel Power MOSFET



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