

Vishay High Power Products

Phase Control SCR, 10 A



PRODUCT SUMMARY				
V_{T} at 6.5 A	< 1.15 V			
I _{TSM}	140 A			
V _{RRM}	800 V			

DESCRIPTION/FEATURES

The 10TTS08 High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification and crow-bar (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

Also available in SMD-220 package (series 10TTS08S).

This product has been designed and gualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS					
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$, common heatsink of 1 °C/W	13.5	17	A		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	6.5	А		
I _{T(RMS)}		10	A		
V _{RRM} /V _{DRM}		800	V		
I _{TSM}		140	А		
V _T	6.5 A, T _J = 25 °C	1.15	V		
dV/dt		150	V/µs		
dl/dt		100	A/µs		
TJ	Range	- 40 to 125	°C		

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA
10TTS08	800	800	1.0

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T 110 °C 100° cond	ution half sing wave	6.5	
Maximum RMS on-state current	I _{T(RMS)}	T _C = 112 °C, 180° condu	iction hall sine wave	10	٨
Maximum peak, one-cycle,	1	10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	120	A
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no volta	age reapplied, $T_J = 125 \ ^{\circ}C$	140	
Movimum 12t for fusing	l ² t	10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	72	A ² s
Maximum I ² t for fusing	1-1	10 ms sine pulse, no volta	age reapplied, $T_J = 125 \ ^{\circ}C$	100	A-S
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no volta	ige reapplied, T _J = 125 °C	1000	A²√s
Maximum on-state voltage drop	V _{TM}	6.5 A, T _J = 25 °C		1.15	V
On-state slope resistance	r _t	- T _J = 125 °C		17.3	mΩ
Threshold voltage	V _{T(TO)}			0.85	V
Maximum reverse and direct lockage autrent	1 /1	T _J = 25 °C		0.05	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$T_J = 125 \degree C$ $V_R = Rated V_{RRM}/V_{DRM}$		1.0	mA
Typical holding current	Ι _Η	Anode supply = 6 V, resistive load, initial $I_T = 1 A$		30	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load		50	
Maximum rate of rise of off-state voltage	dV/dt	T _J = 25 °C		150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P_{GM}		8.0	W	
Maximum average gate power	P _{G(AV)}		2.0	vv	
Maximum peak positive gate current	+I _{GM}		1.5	А	
Maximum peak negative gate voltage	-V _{GM}		10	V	
	I _{GT}	Anode supply = 6 V, resistive load, T_J = - 65 °C	20	mA	
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	15		
		Anode supply = 6 V, resistive load, $T_J = 125 \ ^{\circ}C$	10		
		Anode supply = 6 V, resistive load, $T_J = -65 \ ^{\circ}C$	1.2		
Maximum required DC gate voltage to trigger	V _{GT}	Anode supply = 6 V, resistive load, $T_J = 25 \degree C$	1	v	
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	0.7	v	
Maximum DC gate voltage not to trigger	V_{GD}	$ T_{J} = 125 \text{ °C}, V_{DRM} = \text{Rated value} \qquad \qquad$			
Maximum DC gate current not to trigger	I _{GD}			mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.8	
Typical reverse recovery time	t _{rr}	T 105 %C	3	μs
Typical turn-off time	t _q	T _J = 125 °C	100	



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approvimate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf ⋅ in)
Marking device			Case style TO-220AC	10TT:	S08











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Fig. 7 - On-State Voltage Drop Characteristics



Initial T_= 125°C

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95222					
Part marking information	http://www.vishay.com/doc?95225				



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