

Vishay High Power Products

### Phase Control SCR, 70 A



PRODUCT SUMMARY		
V <sub>T</sub> at 100 A	< 1.4 V	
I <sub>TSM</sub>	1400 A	
V <sub>RRM</sub>	1200/1600 V	

#### DESCRIPTION/FEATURES

The 70TPS.. High Voltage Series of silicon controlled rectifiers are specifically designed for high and medium power switching and phase control applications.

Typical applications are in input rectification (soft start) or AC-switches or high current crow-bar as well as others phase-control circuits.

These products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I <sub>T(AV)</sub>	Sinusoidal waveform	70	А		
I <sub>RMS</sub>	Lead current limitation	75	A		
V <sub>RRM</sub> /V <sub>DRM</sub>	Range	1200/1600	V		
I <sub>TSM</sub>		1400	А		
V <sub>T</sub>	100 A, T <sub>J</sub> = 25 °C	1.4	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ		- 40 to 125	°C		

VOLTAGE RATINGS					
PART NUMBER	V <sub>RRM</sub> /V <sub>DRM</sub> , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> ∕I <sub>DRM</sub> AT 125 °C mA		
70TPS12	1200	1300	- 15		
70TPS16	1600	1700	15		

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ABSOLUTE MAXIMUM RATIN	GS					
PARAMETER	SYMBOL		TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	T <sub>C</sub> = 82 °C, 180° c	conduction half sine w	ave	70	
Maximum continuous RMS on-state current as AC switch	I <sub>T(RMS)</sub>	Lead current limita	Lead current limitation		75	A
Maximum peak, one-cycle	1	10 ms sine pulse,	rated V <sub>RRM</sub> applied		1200	
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine pulse,	10 ms sine pulse, no voltage reapplied		1400	
Maximum 1 <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse,	rated V <sub>RRM</sub> applied	Initial T <sub>J</sub> = T <sub>J</sub> maximum	7200	A <sup>2</sup> s
Maximum I <sup>2</sup> t for fusing	1-1	10 ms sine pulse, no voltage reapplied		10 200	A∸s	
Maximum I²√t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		102 000	A²√s	
Low level value of threshold voltage	V <sub>T(TO)1</sub>	T <sub>J</sub> = 125 °C			0.916	v
High level value of threshold voltage	V <sub>T(TO)2</sub>				1.21	
Low level value of on-state slope resistance	r <sub>t1</sub>	- 1J = 125 C			4.138	
High level value of on-state slope resistance	r <sub>t2</sub>			3.43	mΩ	
Maximum peak on-state voltage	V <sub>TM</sub>	100 A, T <sub>J</sub> = 25 °C		1.4	V	
Maximum rate of rise of turned-on current	dl/dt	T <sub>J</sub> = 25 °C		150	A/µs	
Maximum holding current	Ι <sub>Η</sub>	– T <sub>J</sub> = 25 °C			200	
Maximum latching current	١L			400		
	1 //	T <sub>J</sub> = 25 °C			1.0	mA
Maximum reverse and direct leakage current	I <sub>RRM</sub> /I <sub>DRM</sub>	T <sub>J</sub> = 125 °C	$T_J = 125 \ ^{\circ}C$ $V_R = Rated \ V_{RRM} / V_{DRM}$ 15 $T_J = 125 \ ^{\circ}C$ 500		15	1
Maximum rate of rise of off-state voltage	dV/dt	T <sub>J</sub> = 125 °C			500	V/µs

TRIGGERING					
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P <sub>GM</sub>	T - 20 up		10	w
Maximum average gate power	P <sub>G(AV)</sub>	T = 30 μs		2.5	vv
Maximum peak gate current	I <sub>GM</sub>			2.5	А
Maximum peak negative gate voltage	- V <sub>GM</sub>			10	
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	T <sub>J</sub> = - 40 °C		4.0	V
		T <sub>J</sub> = 25 °C	Anode supply = 6 V resistive load	1.5	
		T <sub>J</sub> = 125 °C		1.1	
		T <sub>J</sub> = - 40 °C		270	
Maximum required DC gate current to trigger	I <sub>GT</sub>	T <sub>J</sub> = 25 °C		100	mA
		T <sub>J</sub> = 125 °C		80	
Maximum DC gate voltage not to trigger	$V_{GD}$	$T_J = 120 \ ^\circ C, V_D$	<sub>PRM</sub> = Rated value	0.25	V
Maximum DC gate current not to trigger	I <sub>GD</sub>			6	mA



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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature	range	TJ		- 40 to 125	- °C	
Maximum storage temperature range		T <sub>Stg</sub>		- 40 to 150		
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	0.27		
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		40	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.2		
Approximate weight				6	g	
Approximate weight				0.21	oz.	
Mounting torque –	minimum			6 (5)	kgf ⋅ cm	
	maximum			12 (10)	(lbf ⋅ in)	
Marking davias			Case style Super-247	70TPS	12	
Marking device			Case signe Super-241	70TPS	16	

SINE HALF WAVE CONDUCTION RECTANGULAR WAVE CONDUCTION				UNITS								
DEVICE         180°         120°         90°         60°         30°         180°	120°	90°	60°	<b>30</b> °	UNITS							
70TPS         0.078         0.092         0.117         0.172         0.302         0.053	0.092	0.125	0.180	0.306	°C/W							

Note

• The table above shows the increment of thermal resistance R<sub>thJ-hs</sub> when devices operate at different conduction angles than DC

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Fig. 3 - On-State Power Loss Characteristics



Fig. 4 - On-State Power Loss Characteristics



Fig. 5 - Maximum Non-Repetitive Surge Current





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Fig. 7 - On-State Voltage Drop Characteristics



Fig. 9 - Thermal Impedance Z<sub>thJC</sub> Characteristics

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#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95073	
Part marking information	http://www.vishay.com/doc?95070	



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