

Vishay High Power Products

Phase Control SCR TO-220AB FULL-PAK, 16 A



PRODUCT SUMMARY		
V _T at 10 A	1.4 V	
I _{TSM}	200 A	
V _{RRM}	800/1200 V	

DESCRIPTION/FEATURES

The 16TTS..FPPbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology



RoHS*

used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

Fully isolated package ($V_{INS} = 2500 V_{RMS}$) is UL E78996 approved **GU**

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$, common heatsink of 1 °C/W	13.5	17	A	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	10	А	
I _{RMS}		16	A	
V _{DRM} /V _{RRM}	Range, for higher voltage up to 1600 V contact factory	800/1200	V	
I _{TSM}		200	А	
V _T	10 A, $T_J = 25 \ ^{\circ}C$	1.4	V	
dV/dt		500	V/µs	
dl/dt		150	A/µs	
TJ	Range	- 40 to 125	°C	

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
16TTS08FPPbF	800	800 10				
16TTS12FPPbF	1200	1200	10			

* Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS				
PARAMETER			VALUES	UNITS
PARAMETER	SYMBOL	TEST CONDITIONS	TYP. MAX.	
Maximum average on-state current	I _{T(AV)}	$T_c = 95 \text{ °C}$, 180° conduction, half sine wave	10	
Maximum RMS on-state current	I _{RMS}		16	
Maximum peak, one-cycle,	1	10 ms sine pulse, rated V _{RRM} applied	170	A
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	200	
Manimum 12t fau funing	l ² t	10 ms sine pulse, rated V _{RRM} applied	144	A ² s
Maximum I ² t for fusing	1-1	10 ms sine pulse, no voltage reapplied	200	A-s
Maximum I ² √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied	2000	A²√s
Maximum on-state voltage drop	V _{TM}	10 A, T _J = 25 °C	1.4	V
On-state slope resistance	r _t	t T _{.1} = 125 °C		mΩ
Threshold voltage	V _{T(TO)}	$I_{\rm J} = 125 {}^{\circ}{\rm C}$	1.1	V
		$T_J = 25 \text{ °C}$	0.5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$V_{\rm R}$ = Rated $V_{\rm RRM}/V_{\rm DRM}$	10	mA
Holding current	Ι _Η	Anode supply = 6 V, resistive load, initial $I_T = 1 / 16TTS08FP$, 16TTS12FP	- 100	
Maximum latching current	١L	Anode supply = 6 V, resistive load	200	mA
Maximum rate of rise of off-state voltage	dV/dt		500	V/µs
Maximum rate of rise of turned-on current	dl/dt		150	A/µs

TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P _{GM}		8.0	w
Maximum average gate power	P _{G(AV)}		2.0	vv
Maximum peak positive gate current	+ I _{GM}		1.5	А
Maximum peak negative gate voltage	- V _{GM}		10	V
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T_J = - 10 °C	90	mA
		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	60	
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	35	
Maximum required DC gate voltage to trigger	V _{GT}	Anode supply = 6 V, resistive load, T_J = - 10 °C	3.0	
		Anode supply = 6 V, resistive load, T_J = 25 °C	2.0	
		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	1.0	v
Maximum DC gate voltage not to trigger	V_{GD}	$T = 125 \degree C V$ = Poted value	0.2]
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value 2		mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	$T_J = 25 \ ^{\circ}C$	0.9	
Typical reverse recovery time	t _{rr}	T _{.1} = 125 °C	4	μs
Typical turn-off time	tq	1 = 125 0	110	



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	1.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque ——	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
Madian device				16TTS08FP	
Marking device			Case style TO-220AB FULL-PAK (94/V0)	16TTS ⁻	I2FP

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Fig. 7 - On-State Voltage Drop Characteristics



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ORDERING INFORMATION TABLE



Note: For higher voltage up to 1600 V contact factory

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95072			
Part marking information	http://www.vishay.com/doc?95069		



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