

V_{RRM}

Q

 $\mathbf{I}_{F(AVG)} = 10 \text{ A}$

= 600 V

= 28 nC

CSD10060-Silicon Carbide Schottky Diode

ZERO RECOVERY® RECTIFIER

Features

- 600-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F

Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

Applications

- Switch Mode Power Supplies
- Power Factor Correction
 - Typical PFC P_{out}: 1000W-2000W
- Motor Drives
 - Typical Power : 3HP-5HP

Package





Part Number	Package	Marking		
CSD10060A	TO-220-2	CSD10060		
CSD10060G	TO-263-2	CSD10060		

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	600	V		
V _{RSM}	Surge Peak Reverse Voltage	600	V		
V _{DC}	DC Blocking Voltage	600	V		
$I_{_{F(AVG)}}$	Average Forward Current	10 16.5	А	T _c =150°C T _c =125°C	
$I_{_{F(Peak)}}$	Peak Forward Current	23	А	$T_{c}=125^{\circ}, T_{REP}<1 \text{ mS, Duty}=0.5$	
I _{frm}	Repetitive Peak Forward Surge Current	43 29	А	$T_c=25$ °C, $t_p=10$ ms, Half Sine Wave $T_c=125$ °C, $t_p=10$ ms, Half Sine Wave	
\mathbf{I}_{FSM}	Non-Repetitive Peak Forward Surge Current	77	A	$T_c=25$ °C, t _p =1.5 ms, Half Sine Wave	
\mathbf{I}_{FSM}	Non-Repetitive Peak Forward Surge Current	250	А	$T_c = 25^{\circ}C$, $t_p = 10 \ \mu s$, Pulse	
P_{tot}	Power Dissipation	136.3 45.4	w	T _c =25°C T _c =125°C	
$T_{_{\mathrm{J}}}$, $T_{_{\mathrm{stg}}}$	Operating Junction and Storage Temperature	-55 to +175	°C		
	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 2.0	1.8 2.4	V	$I_F = 10 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 10 \text{ A } T_J = 175^{\circ}\text{C}$	
I _R	Reverse Current	50 100	200 1000	μA	$V_{R} = 600 V T_{J} = 25^{\circ}C$ $V_{R} = 600 V T_{J} = 150^{\circ}C$	
Q _c	Total Capacitive Charge	28		nC	$V_{R} = 600 V, I_{F} = 10 A$ $di/dt = 500 A/\mu s$ $T_{J} = 25^{\circ}C$	
С	Total Capacitance	550 65 50		pF	$V_{R} = 0 V, T_{J} = 25^{\circ}C, f = 1 MHz$ $V_{R} = 200 V, T_{J} = 25^{\circ}C, f = 1 MHz$ $V_{R} = 400 V, T_{J} = 25^{\circ}C, f = 1 MHz$	

Note:

1. This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	Parameter	Тур.	Unit
$R_{_{\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!\!$	Thermal Resistance from Junction to Case	1.1	°C/W

Typical Performance













Figure 4. Capacitance vs. Reverse Voltage



Figure 5. Transient Thermal Impedance



Typical Performance



Figure 6. Power Derating



Package Dimensions

Package TO-220-2





m



Package Dimensions

Package TO-263-2





	POS		hes	Millimeters	
	P05	Min	Max	Min	Мах
	А	.381	.410	9.677	10.414
	В	.235	.255	5.969	6.477
	С	.100	.120	2.540	3.048
	D	.223	.337	5.664	8.560
	E	.590	.615	14.986	15.621
X	F	.143	.153	3.632	3.886
	G	1.105	1.147	28.067	29.134
	Н	.500	.550	12.700	13.970
	J	R 0.197		R 0.197	
	L	.025	.036	.635	.914
	М	.045	.055	1.143	1.397
	N	.195	.205	4.953	5.207
	Р	.165	.185	4.191	4.699
	Q	.048	.054	1.219	1.372
	S	3°	6°	3°	6°
	Т	3°	6°	3°	6°
	U	3°	6°	3°	6°
	V	.094	.110	2.388	2.794
	W	.014	.025	.356	.635
	Х	3°	5.5°	3°	5.5°
	Y	.385	.410	9.779	10.414
	z	.130	.150	3.302	3.810

NOTE:

1. Dimension L, M, W apply for Solder Dip Finish

POS	Inc	hes	Millimeters		
PUS	Min	Max	Min	Max	
A	.396	.406	10.058	10.312	
В	.297	.303	7.544	7.696	
С	.057	.063	1.448	1.600	
D	.237	.243	6.015	6.167	
E*	0.00	.070	0.00	1.778	
F	.048	.062	1.219	1.575	
G	.100	ТҮР	2.540	0 TYP	
н	.335	.345	8.509	8.763	
J	.028	.034	.711	.864	
К	2°	4°	2°	4°	
L	.170	.180	4.318	4.572	
м	.048	.052	1.219	1.321	
N	.595	.615	15.113	15.621	
Р	0.00	0.10	0.00	.254	
Q	R0.018 TYP	R0.022 TYP	R0.457 TYP	R0.559 TYP	
R	.090	.110	2.286	2.794	
S	.013	.017	.330	.432	
Т	6.5°	8.5°	6.5°	8.5°	
U	.103	.107	2.616	2.718	
V	R0.028 TYP	R0.032 TYP	R0.711 TYP	R0.813 TYP	
W	-	5.0°	_	5.0°	

Note:

* Tab "E" may not be present



Recommended Solder Pad Layout



Part Number	Package	Marking
CSD10060A	TO-220-2	CSD10060
CSD10060G	TO-263-2	CSD10060

Diode Model



 $Vf_T = V_T + If^*R_T$

 $\begin{array}{l} V_{T^{=}}0.92+(T_{j}*\text{-}1.35*10^{\text{-}3})\\ R_{T^{=}}0.052+(T_{j}*0.29*10^{\text{-}3}) \end{array}$

Note: T_i = Diode Junction Temperature In Degrees Celcius

"The levels of environmentally sensitive, persistent biologically toxic (PBT), persistent organic pollutants (POP), or otherwise restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2002/95/EC on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS), as amended through April 21, 2006. This part number was released previously with Sn/Pb solder plating as a standard industry finish. For more information please contact power_sales@cree.com "

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.

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