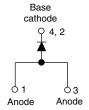


Vishay Semiconductors

Schottky Rectifier, 3.0 A





D-PAK	(TO-252AA)
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PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I _{F(AV)}	3.0 A					
V _R	20 V, 30 V, 40 V					
V _F at I _F	0.49 V					
I _{RM}	20 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Single die					
E _{AS}	8 mJ					

FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable



- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

DESCRIPTION

The VS-MBRD320PbF, VS-MBRD330PbF, VS-MBRD340PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	3.0	А						
V _{RRM}		20 to 40	V						
I _{FSM}	t _p = 5 μs sine	490	А						
V _F	3 Apk, T _J = 125 °C	0.49	V						
T _J		- 40 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-MBRD320PbF	VS-MBRD330PbF	VS-MBRD340PbF	UNITS			
Maximum DC reverse voltage	V_{R}	20	30	40	V			
Maximum working peak reverse voltage	V_{RWM}	20	30	40	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL TEST CONDITIONS							
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 133 °C, re	3.0					
Maximum peak one cycle non-repetitive surge current	l	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	490	Α			
	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	75				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 16 \text{mH}$	8.0	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero i Frequency limited by T _J maximum	1.0	А				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS			
		3 A	T _J = 25 °C	0.48	0.6	V		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	6 A	1] = 23 0	0.58	0.7			
See fig. 1		3 A	T _{.I} = 125 °C	0.41	0.49			
		6 A	1J = 125 C	0.55	0.625			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V_{R} = Rated V_{R}	0.02	0.2	- mA		
See fig. 2		T _J = 125 °C	v _R = nateu v _R	10.7	20			
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal ran	189	-	pF			
Typical series inductance	L _S	Measured lead to lead 5 n	5.0	-	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	-	10 000	V/µs			

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	T _J ⁽¹⁾		- 40 to 150	°C				
Maximum storage temperature range	T _{Stg}		- 40 to 175	C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	6.0	°C/W				
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV				
Approximate weight			0.3	g				
Approximate weight			0.01	OZ.				
			MBRD320					
Marking device		Case style D-PAK (similar to TO-252AA)	MBRD330					
			MBRD340					

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$





Schottky Rectifier, 3.0 A

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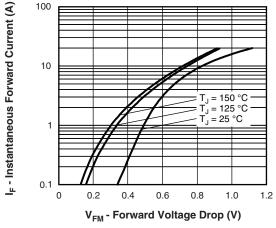


Fig. 1 - Maximum Forward Voltage Drop Characteristics

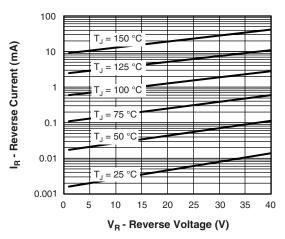


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

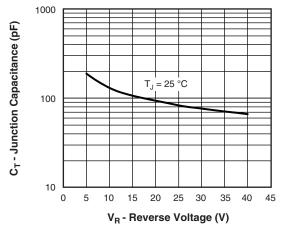


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

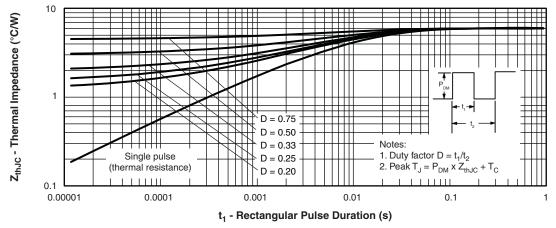


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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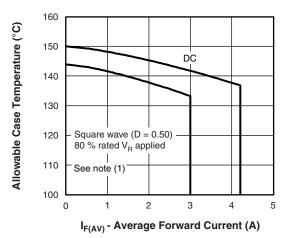


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

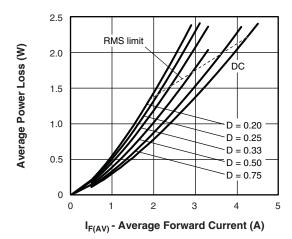


Fig. 6 - Forward Power Loss Characteristics

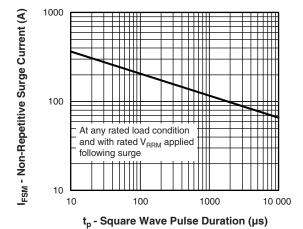


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

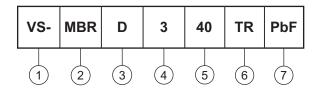


Schottky Rectifier, 3.0 A

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Schottky MBR series

D = TO-252AA (D-PAK)

- Current rating (3 = 3 A)

20 = 20 V 30 = 30 V

Voltage ratings =

40 = 40 V

None = Tube (50 pieces)TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

7 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95016						
Part marking information	www.vishay.com/doc?95059						
Packaging information	www.vishay.com/doc?95033						



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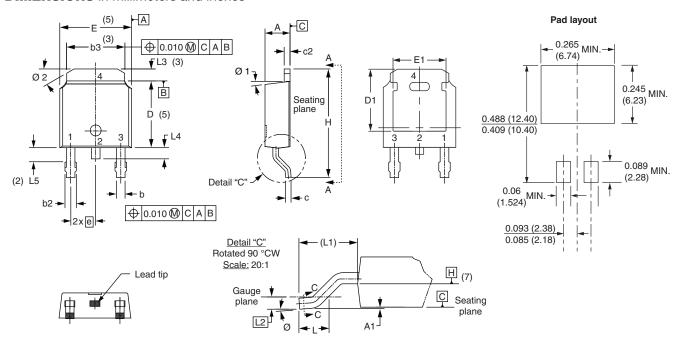
NOTES

3

2

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	NOTES	NOTES	S NOTES		SYMBOL	MILLIN	IETERS	INC	HES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.				
Α	2.18	2.39	0.086	0.094			e 2.29 BSC 0.090 BSC		BSC						
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410				
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070				
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.				
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC				
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050				
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040				
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060				
D1	5.21	-	0.205	1	3		Ø	0°	10°	0°	10°				
Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°				
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°				

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA



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