

Vishay High Power Products

Schottky Rectifier, 2 x 8 A



PRODUCT SUMMARY					
I _{F(AV)} 2 x 8 A					
V _R	60 to 100 V				

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES						
I _{F(AV)}	Rectangular waveform	16	A					
V _{RRM}		60 to 100	V					
I _{FSM}	t _p = 5 μs sine	850	A					
V _F	8 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.58	V					
TJ	Range	- 55 to 175	۵°C					

VOLTAGE RATINGS							
PARAMETER	SYMBOL	16CTQ060S 16CTQ060-1	16CTQ080S 16CTQ080-1	16CTQ100S 16CTQ100-1	UNITS		
Maximum DC reverse voltage	V _R	60	80	100	М		
Maximum working peak reverse voltage	V _{RWM}	00	00	100	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS		UNITS		
Maximum average per le	· .	50 % duty cycle at $T_{\rm C}$ = 148 °C, rectangular waveform		8	А		
See fig. 5 per devic	I _{F(AV)}	30% duty cycle at $10 = 140$ C	16	~			
Maximum peak one cycle non-repetitive surge current per leg		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	850	А		
See fig. 7	ive surge current per leg I _{FSM}		V_{RRM} applied	275	~		
Non-repetitive avalanche energy per leg E _{AS}		T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		0.50	А		

16CTQ...S/16CTQ...-1 Series

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		8 A	T.I = 25 °C	0.72	v	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	16 A	$1_{\rm J} = 25^{\circ}{\rm C}$	0.88		
See fig. 1	VFM ('')	8 A	T.I = 125 °C	0.58		
		16 A	lj = 125 °C	0.69		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	$V_{\rm B}$ = Rated $V_{\rm B}$	0.55	mA	
See fig. 2		T _J = 125 °C	V _R = naleu V _R	7.0		
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.415	V	
Forward slope resistance	r _t			11.07	mΩ	
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C 500			pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0 n			nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHAN	IICAL SP	ECIFICA	ATIONS			
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		Б		3.25		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50]	
Approximate weight				2	g	
				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
				16CTC	2060S	
			Case style D ² PAK	16CTQ080S		
				16CT0	16CTQ100S	
Marking device				16CTC	060-1	
			Case style TO-262	16CTC	16CTQ080-1	
				16CTC	2100-1	



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V_R - Reverse Voltage (V)

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)



Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \ \mathsf{applied} \end{array}$



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ORDERING INFORMATION TABLE

Device code	16	С	т	Q	100	S	TRL	-	
	1	2	3	4	5	6	7	8	
	1 - 2 -	Circ	uit conf	ng (16 A iguratior	n				
	3 - 4 - 5 - 6 -	T = Sch Voli	TO-220)" series ngs —		(060 = 60 080 = 80 00 = 10	o v 🛛	
	7 -	• N • TI	RL = Ta	ube (50 pe and r	reel (left	oriente			
	8 -	• N	one = S	ape and tandard ad (Pb)-	product		itea - Toi	Γυ-ΡΑκ	oniy)

LINKS TO RELATED DOCUMENTS					
Dimensions	http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008				
Packaging information	http://www.vishay.com/doc?95032				
SPICE model	http://www.vishay.com/doc?95279				



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