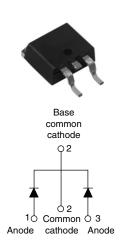


#### Vishay High Power Products

# HEXFRED® Ultrafast Soft Recovery Diode, 2 x 8 A



 $D^2PAK$ 

PRODUCT SUMMARY				
$V_{R}$	600 V			
V <sub>F</sub> at 8 A at 25 °C	1.7 V			
I <sub>F(AV)</sub>	2 x 8 A			
t <sub>rr</sub> (typical)	18 ns			
T <sub>J</sub> (maximum)	150 °C			
Q <sub>rr</sub>	65 nC			
dI <sub>(rec)M</sub> /dt	240 A/μs			

#### **FEATURES**

- Ultrafast recovery
- · Ultrasoft recovery
- Very low I<sub>RRM</sub>
- Very low Q<sub>rr</sub>
- · Specified at operating conditions
- · Designed and qualified for industrial level

#### **BENEFITS**

- · Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION**

HFA16TA60CS is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 8 A per leg continuous current, the HFA16TA60CS is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I<sub>RBM</sub>) and does not exhibit any tendency to "snap-off" during the t<sub>h</sub> portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA16TA60CS is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V <sub>R</sub>		600	V	
Maximum continuous forward current per leg per device	- I <sub>F</sub>	T <sub>C</sub> = 100 °C	8		
			16	Α	
Single pulse forward current	I <sub>FSM</sub>		60	A	
Maximum repetitive forward current	I <sub>FRM</sub>		24		
Maximum power dissipation	$P_{D}$	T <sub>C</sub> = 25 °C	36	W	
waxiinum power dissipation		T <sub>C</sub> = 100 °C	14	]	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to + 150	°C	

### **HFA16TA60CS**

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<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	Ι <sub>R</sub> = 100 μΑ		600	-	-	
	V <sub>FM</sub>	I <sub>F</sub> = 8.0 A		-	1.4	1.7	V
Maximum forward voltage		I <sub>F</sub> = 16 A	See fig. 1	-	1.7	2.1	
		I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 125 °C		-	1.4	1.7	
Maximum reverse		$V_R = V_R$ rated	See fig. 2	-	0.3	5.0	μΑ
leakage current	I <sub>RM</sub>	$T_J = 125 ^{\circ}\text{C},  V_R = 0.8  \text{x}  V_R  \text{rated}$		-	100	500	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	See fig. 3	=	10	25	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		=	8.0	=	nH

<b>DYNAMIC RECOVERY CHARACTERISTICS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	18	-	
Reverse recovery time See fig. 5, 6 and 16	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C		-	37	55	ns
occ lig. 5, 6 and 16	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	55	90	
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C	$I_F = 8.0 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	3.5	5.0	А
See fig. 7 and 8	I <sub>RRM2</sub>	T <sub>J</sub> = 125 °C		-	4.5	8.0	
Reverse recovery charge	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C		-	65	138	nC
See fig. 9 and 10	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	124	360	110
Peak rate of fall of recovery current during t <sub>b</sub> See fig. 11 & 12	dI <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C		-	240	-	- A/μs
	dI <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C		-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Junction to case, single leg conducting	В		-	-	3.5	
Junction to case, both legs conducting	R <sub>thJC</sub>		-	-	1.75	K/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	80	
Woight			-	2	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D <sup>2</sup> PAK		HFA16	TA60CS	•



## HEXFRED® Vishay High Power Products Ultrafast Soft Recovery Diode, 2 x 8 A

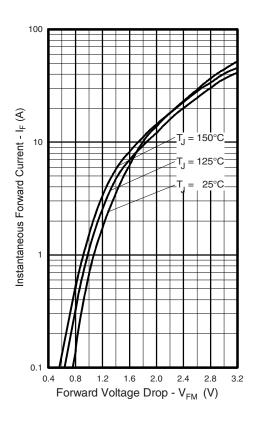


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)

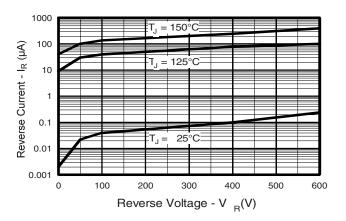


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

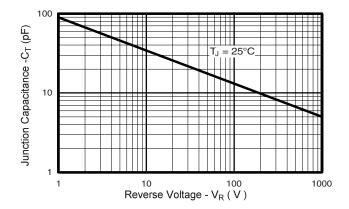


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

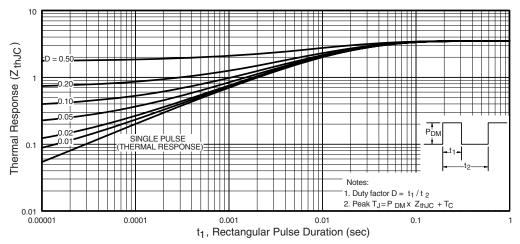


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics (Per Leg)

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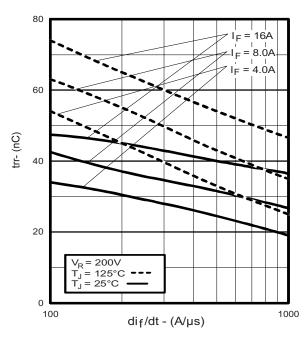


Fig. 5 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt (Per Leg)

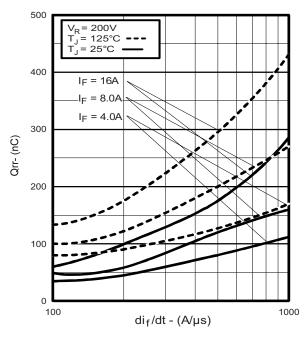


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt (Per Leg)

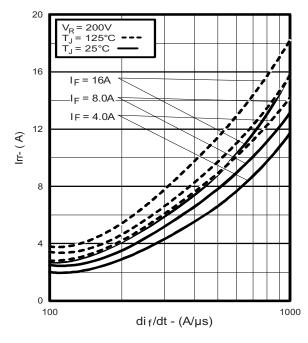


Fig. 6 - Typical Recovery Current vs. dI<sub>F</sub>/dt (Per Leg)

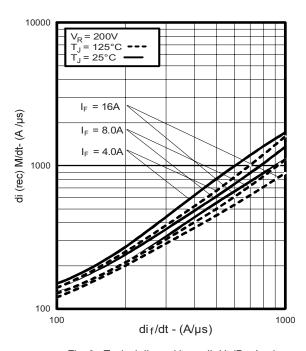


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$  (Per Leg)



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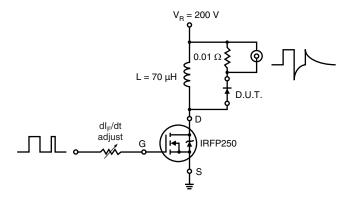
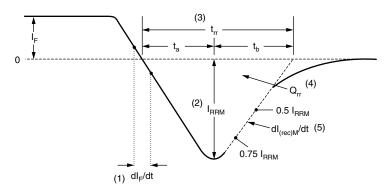


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{r}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times l_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95046				
Part marking information	http://www.vishay.com/doc?95054			
Packaging information	http://www.vishay.com/doc?95032			



Vishay

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Document Number: 91000 Revision: 18-Jul-08