

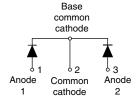
Vishay High Power Products

COMPLIANT

Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

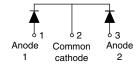
VS-115CNQ015APbF





VS-115CNQ015ASMPbF



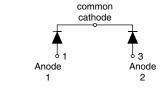


Base

D-61-8-SM

VS-115CNQ015ASLPbF





D-61-8-SL

PRODUCT SUMMARY					
I _{F(AV)}	2 x 55 A				
V _R at T _J = 100 °C	15 V				

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Center tap module
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- New fully transfer-mold low profile, small footprint, high current package
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	110	А		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 µs sine	5050	А		
V _F	55 Apk, T _J = 75 °C (per leg)	0.33	V		
T _J	Range	- 55 to 125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VS-115CNQ015APbF	UNITS	
Maximum DC reverse voltage	V_{R}	T _J = 100 °C	15	V	
Maximum working peak reverse voltage	V_{RWM}	T _J = 125 °C	5	V	

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

VS-115CNQ015A PbF Series



Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A



ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYME	BOL	. TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	I _{F(AV)} 50 % duty cycle at T _C = 112 °C, rectangular waveform		55	Α	
	device I _{F(A\}	/)	30 % duty cycle at 1°C = 112 °C, rectangular wavelonn		110	ζ
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and	5050	٨
non-repetitive surge current per leg See fig. 7	IFSN	Л	10 ms sine or 6 ms rect. pulse		830	Α
Non-repetitive avalanche energy per leg		3	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 4.5 \text{mH}$		54	mJ
Repetitive avalanche current per leg I _{AI}		l	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 3$ x V_R typical		2	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	55 A	T _{.1} = 25 °C	0.37	V
Maximum forward voltage drop per leg		110 A	11 = 25 0	0.46	
See fig. 1		55 A	T _{.1} = 75 °C	0.33	
		110 A	1j=75 C	0.43	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	20	mA
		T _J = 100 °C	v _R = nateu v _R	1200	
		T _J = 100 °C	V _R = 12 V	900	IIIA
		T _J = 100 °C	V _R = 5 V	540	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5500	pF
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body 5.5		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _k		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		TJ		- 55 to 125	°C
Maximum storage temperat	ure range	T _{Stg}		- 55 to 150	C
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation See fig. 4	0.5	°C/W
			DC operation	0.25	
Typical thermal resistance, case to heatsink (D-61-8 on	ly)	R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight	Approximate weight			7.8	g
Approximate weight				0.28	OZ.
Mounting torque	minimum			40 (35)	kgf · cm
(D-61-8 only)	maximum			58 (50)	(lbf \cdot in)
Marking device			Case style D-61	115CN	Q015A
			Case style D-61-8-SM	115CNQ	015ASM
			Case style D-61-8-SL	115CNQ	015ASL

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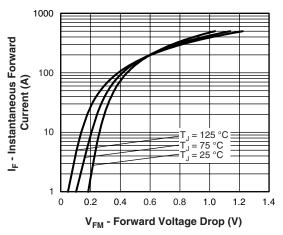


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

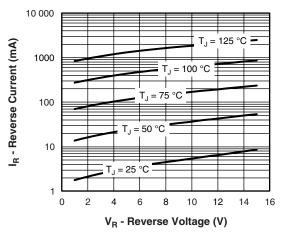


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

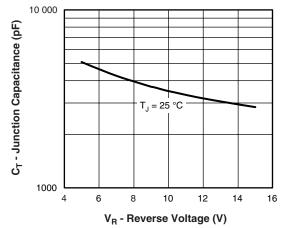


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

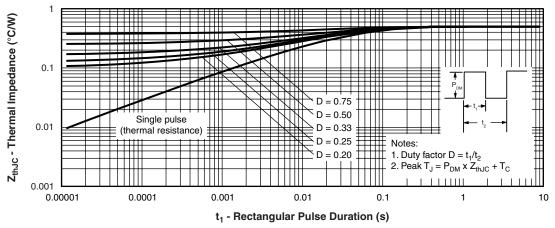


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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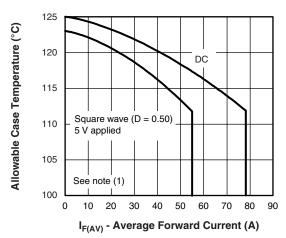


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

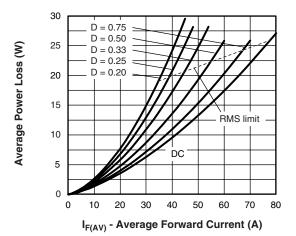


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

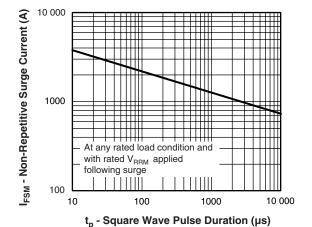


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

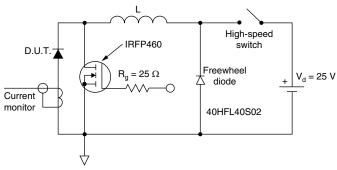


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 5 V

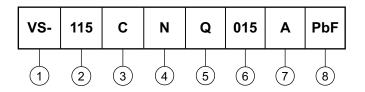


VS-115CNQ015A PbF Series

Schottky Rectifier Vishay High Power Products New Generation 3 D-61 Package, 2 x 55 A

ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

Current rating (110 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage rating (015 = 15 V)

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = Standard production

• PbF = Lead (Pb)-free

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95354				
Part marking information	www.vishay.com/doc?95356				

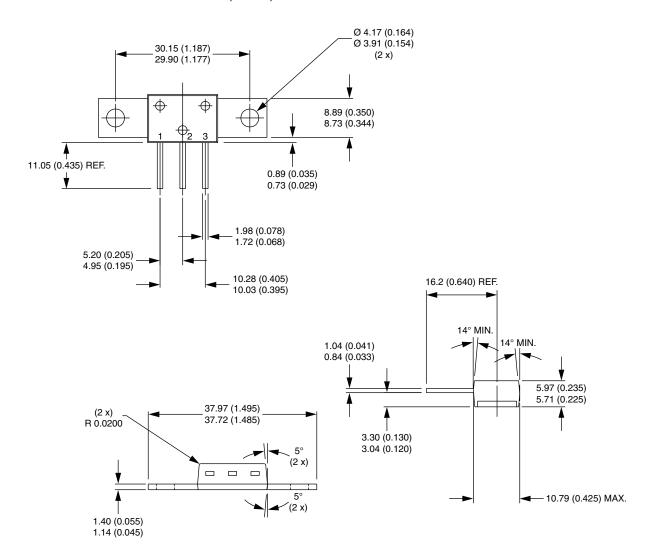
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Vishay Semiconductors

D-61-8, D-61-8-SM, D-61-8-SL

DIMENSIONS - D-61-8 in millimeters (inches)

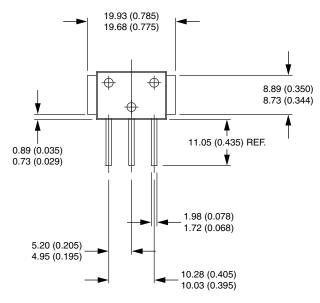


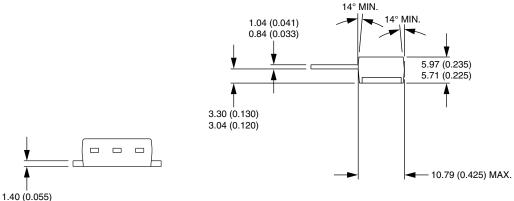


Vishay Semiconductors

DIMENSIONS - D-61-8-SM in millimeters (inches)

1.14 (0.045)

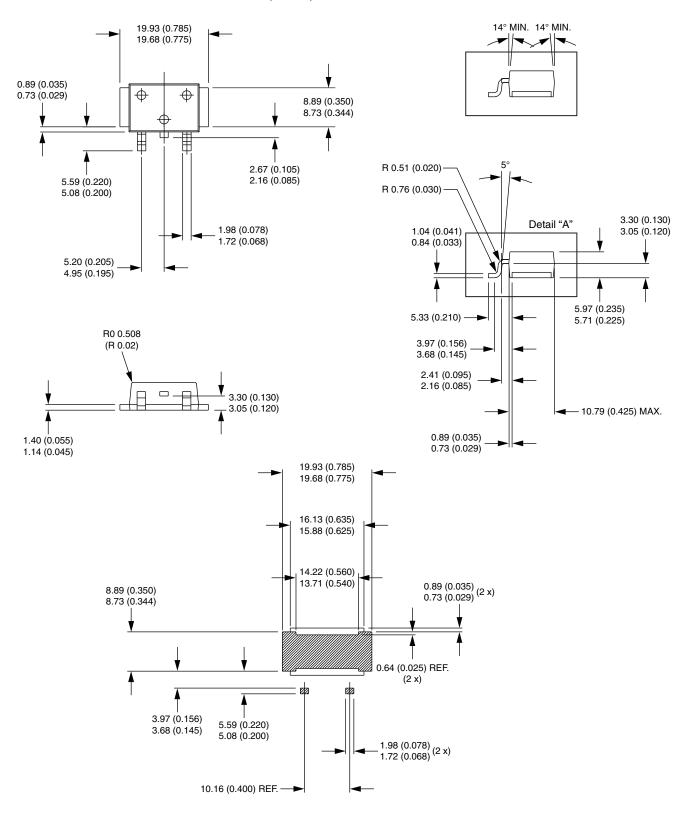






Vishay Semiconductors

DIMENSIONS - D-61-8-SL in millimeters (inches)





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